Network Master™ Series

Network Master Flex  MT1100A

10G Multirate Module  MU110010A
100G Multirate Module  MU110011A
40/100G Advanced Module  MU110013A
Versatile Support up to 100 Gbps plus High Work Efficiency at R&D and Mass-Production Manufacturing

Today’s core and metro communications networks are implementing 100 GigE and OTN technologies rapidly to provide sufficient bandwidth supporting the explosive increase in mobile communications data. These high-bit-rate networks demand very high reliability due to the large data volumes and variety of client signals in use. Consequently, every stage from R&D through to manufacturing, installation, and maintenance, requires precision testing and verification of network equipment and transport devices.

The all-in-one Network Master Flex MT1100A supports the communications network technologies. Selecting and installing up to two modules from a range of three module options supports all-in-one R&D, manufacturing, installation and maintenance tests of network and transport equipment operating at bit rates from 1.5 Mbps to 100 Gbps. The large, 12.1-inch color LCD touch panel with easy-to-use GUI plus remote operation of a full range of test functions over an Internet connection greatly improves test efficiency and helps cut costs.

Key Benefits and Features:
- All-in-one transport tester
- Supports testing from 1.5 Mbps to 100 Gbps
- Up to 4 ports at all rates
- Various interfaces for optical transceivers support: CFP, CFP2, CFP4, CXP, QSFP28, QSFP+, SFP+, SFP
- Various interfaces for electrical support: CAUI, XLAUI, CAUI4
- Easy and intuitive GUI
- WLAN/Bluetooth/LAN connectivity
- PDF, CSV and XML report generation for documenting test results
- Remote operation (VNC, Dedicated GUI software)
- Remote control (scripting, via Ethernet, WLAN, GPIB)
- Compact, lightweight design for maximum field portability
- High performance in small form factor
- Modular platform ensuring maximum return on investment

Key Applications:
- Core and metro networks installation and maintenance
- OTN up to OTU4 including mapping of Ethernet, CPRI, Fibre Channel and SDH/SONET client signals, multistage mapping and FEC (Forward Error Correction) also supporting O.182 Poisson error addition
- Testing and verification of OTN functions ODU0, ODU2e, ODU3e1, ODU3e2, ODU4, ODUsflex
- Carrier Class Ethernet installation and troubleshooting
- Ethernet testing up to 100 Gbps including RFC 2544, Y.1564 and RFC 6349 (Up to 10 Gbps)
- 100GBASE-SR4 RS-FEC
- Ethernet OAM
- MPLS-TP and PBB
- IP Channel Statistics (up to 10 Gbps)
- Frame capture for advanced troubleshooting
- Mobile Fronthaul and backhaul installation and verification
- Synchronous Ethernet testing up to 10 Gbps (ITU-T G.826x, IEEE 1588 v2)
- TCP Throughput testing with RFC 6349 or iperf
- Powerful Storage Area Networking (SAN) testing
- Fibre Channel up to 10 Gbps
- Quick and easy testing of SDH/SONET and PDH/DSn networks
- SDH/SONET (STM-1 to 256/OC-3 to 768)
- PDH/DSn (E1, E3, E4, DS1, DS3)

*1: Available for certified countries and regions including USA, Canada, Japan and EU countries. Please visit the Anritsu web site for updated information.

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Connector Panel Overview

Power Supply Module
- Battery and AC Power Supply Module MU110001A
- AC only High Power Supply Module MU110002A

Test Module (Select one or two modules)
- 10G Multirate Module MU110010A
- 100G Multirate Module MU110011A
- 40/100G Advanced Module MU110013A

Mainframe
- Network Master Flex MT1100A

1. Unit Sync. Input (for future use)
2. Unit Sync. Output (for future use)
3. Audio (3.5ø: CTIA Standard)
4. AUX (for G0325A, GPS receiver)
5. External Clock Input
6. USB Mini-B
7. USB A
8. USB A
9. Ethernet Service Interface
Measurement Modules

Transport Tester for Evolving (Advancing) Networks

10G Multirate Module  MU110010A

1. Tx Reference Clock Output
2. Port1, Tx Mini-bantam (DS1)
3. Port1, Tx BNC (E1, E3, E4, DS3, STM-1e, STS-3)
4. Port1, Rx Mini-bantam (DS1)
5. Port1, Rx BNC (E1, E3, E4, DS3, STM-1e, STS-3)
6. Port2, Tx BNC (DS1)
7. Port2, Tx BNC (E1, E3, E4, DS3, STM-1e, STS-3)
8. Port2, Rx Mini-bantam (DS1)
9. Port2,Rx BNC (E1, E3, E4, DS3, STM-1e, STS-3)
10. Port1, Tx/Rx RJ48 (E1 balanced)
11. Port2, Tx/Rx RJ48 (E1 balanced)
12. Port1, Tx/Rx SFP/SFP+ (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET optical)
13. Port2, Tx/Rx SFP/SFP+ (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET optical)
14. Port1, Tx/Rx RJ45 (Ethernet electrical)
15. Port2, Tx/Rx RJ45 (Ethernet electrical)

100G Multirate Module  MU110011A

16. Port1, Tx/Rx RJ45 (Ethernet electrical)
17. Port2, Tx/Rx RJ45 (Ethernet electrical)
18. Tx Reference Clock Output
19. AUX Input (for future use)
20. Tx/Rx CFP (OTN, Ethernet, SDH/SONET optical)
21. Port1, Tx/Rx QSFP+ (OTN, Ethernet optical)
22. Port2, Tx/Rx QSFP+ (OTN, Ethernet optical)
23. Port1, Tx/Rx SFP/SFP+ (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET optical)
24. Port2, Tx/Rx SFP/SFP+ (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET optical)
25. Act, Link Indicators

40/100G Advanced Module  MU110013A

26. Port1, CFP2 Sync. Clock Output
27. Port2, CFP2 Sync. Clock Output
28. Tx Reference Clock Output
29. AUX Input (for future use)
30. Port1, Tx/Rx CFP2 (OTN, Ethernet optical)
31. Port1, Tx/Rx QSFP+ (OTN Ethernet optical)
32. Port2, Tx/Rx QSFP+ (OTN Ethernet optical)
33. Port2, Tx/Rx CFP2 (OTN, Ethernet optical)
34. Port2, Tx/Rx CXP (Ethernet optical)
35. Port2, Tx/Rx QSFP+ (Ethernet optical)
36. Act, Link Indicators
**Mainframe and Modules**

<table>
<thead>
<tr>
<th>Product Number</th>
<th>Product Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>MT1100A</td>
<td>Network Master Flex Mainframe</td>
<td></td>
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<tr>
<td>MU110001A</td>
<td>Battery and AC Power Supply Module</td>
<td>Power supply module for MT1100A Includes G0237A x 2 (Battery), Z1862A (Hexagon wrench)</td>
</tr>
<tr>
<td>MU110002A</td>
<td>AC only High Power Supply Module</td>
<td>High power supply module for MT1100A Includes Z1862A (Hexagon wrench)</td>
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<tr>
<td>MU11010A</td>
<td>10G Multirate Module</td>
<td>SFP/SFP+: 2, RJ45: 2, BNC (Tx/Rx): 2, RJ48: 2, Mini-bantam (Tx/Rx): 2</td>
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<tr>
<td>MU110011A</td>
<td>100G Multirate Module</td>
<td>CFP: 1, QSFP+: 2, SFP/SFP+: 2, RJ45: 2</td>
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<tr>
<td>MU110013A</td>
<td>40/100G Advanced Module</td>
<td>CFP2: 2, CXP: 2, QSFP+: 2, CFP4: 2 (with J1665A), QSFP28: 2 (with J1686B)</td>
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**Power Supply Modules and Test Modules Combination**

**Battery and AC Power Supply Module MU110001A**

<table>
<thead>
<tr>
<th>Module 2</th>
<th>No Module</th>
<th>MU110010A</th>
<th>MU110011A</th>
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<td>✓</td>
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**AC only High Power Supply Module MU110002A**

<table>
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<th>MU110011A</th>
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✓: Available — : Not Available

**Test Modules and Maximum Operating Ports**

<table>
<thead>
<tr>
<th>Protocol</th>
<th>PDH/DSn</th>
<th>OTU1</th>
<th>100 Mbps to 1 Gbps Ethernet</th>
<th>STM-16/OC-48</th>
<th>GbE to 4 GbE</th>
<th>OTU2/1e/1f/2f</th>
<th>10 Gbps Ethernet</th>
<th>STM-64/OC-192</th>
<th>8 Gbps to 10 Gbps Ethernet</th>
<th>QSBA1 X to 4 X</th>
<th>GFP Option 1 to 8</th>
<th>OTU3/2e/2e/3e</th>
<th>OTU4</th>
<th>40 Gbps Ethernet</th>
<th>STM-256/OC-768</th>
<th>100 Gbps Ethernet</th>
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<tbody>
<tr>
<td>MU110010A</td>
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<td>2 ports</td>
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</table>

*1: Up to two ports in two QSFP+ and one CFP can be operated simultaneously.

*2: MU110013A does not have a STM-256/OC-768 physical interface.

MU110013A-083/084 are the options for STM-256/OC-768 client signals mapped in the OTN. Please refer to page 7.
Support the mappings of OTU1 and OTU2x in MU110010A

Support the mappings of OTU1 and OTU2x in MU110011A
Support the mappings of OTU3 in MU110011A/13A

OTN Mappings

Not supported mappings on 2 port mode
- MU110011A-061 ODU Multiplexing
- MU110011A-063 40/100G ODU Multistage
- MU110011A-062 ODU Flex
- MU110011A-063 40/100G ODU Multistage
- MU110011A/13A-003/001 Up to 10G Single/Dual Channel

- MU110011A-005/004 OTN 40 Gig Single/Dual Channel
- MU110011A-061 ODU Multiplexing
- MU110011A/13A-063 40/100G ODU Multistage
- MU110011A-062 ODU Flex
- MU110011A/13A-063 40/100G ODU Multistage
- MU110011A/13A-003/001 Up to 10G Single/Dual Channel

- MU110011A-071/072 ODU Single/Dual Channel
- MU110011A/13A-005/004 OTN 40 Gig Single/Dual Channel
- MU110011A/13A-061 ODU Multiplexing
- MU110011A/13A-062 ODU Flex
- MU110011A/13A-063 40/100G ODU Multistage
- MU110011A/13A-003/001 Up to 10G Single/Dual Channel

- MU110011A-071/072 CPRI/OBSAI Up to 10G Single/Dual Channel
- MU110011A-071/072 CPRI Up to 10G Single/Dual Channel
- MU110011A-071/072 CPRI Opt. 2/1
- MU110011A/13A-061 ODU Multiplexing
- MU110011A/13A-062 ODU Flex
- MU110011A/13A-063 40/100G ODU Multistage
- MU110011A/13A-003/001 Up to 10G Single/Dual Channel
OTN Mappings

Support the mappings of OTU4 in MU110011A/13A

SDH/SONET Mappings

Supported at one port usage
- MU110011A/13A-055/056 OTU4 in MU110011A/13A 100G Single/Dual Channel
- MU110011A-061 ODU Multiplexing
- MU110011A/13A-062 ODU Flex
- MU110011A/13A-063 40/100G ODU Multistage
- MU110011A/13A-003/001 Up to 10G Single/Dual Channel
- MU110011A/13A-005/004 Up to 10G FC Single/Dual Channel
- MU110011A-071/072 CPRI/OBSAI Up to 10G Single/Dual Channel
- MU110013A-071/072 CPRI Up to 10G Single/Dual Channel
- MU110011A/13A-015/016 Ethernet 100G Single/Dual Channel
- MU110011A/13A-013/014 Ethernet 40G Single/Dual Channel
- MU110011A/13A-083/084 STM-256 OC-768 Single/Dual Channel
- STM-256  OC-64  VC-4-4c  VC-3  C-3  STS-3cSPE  Bulk  E4
- STM-64  OC-192  VC-4-256c  STS-768c  Bulk  E3
- STM-16  OC-48  VC-4-64c  STS-192c  Bulk  DS3
- STM-4  OC-24  VC-4-16c  STS-48c  Bulk  E3
- STM-1  OC-12  VC-4  STS-12c  Bulk  E1
- STM-48c  STS-12cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
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- STM-192cSPE  Bulk  DS1
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- STM-192cSPE  Bulk  DS1
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- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
- STM-48c  STS-48cSPE  Bulk  DS1
- STM-192c  OC-768cSPE  Bulk  DS1
- STM-192cSPE  Bulk  DS1
## Optical Transceivers Specification

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Description (Approx. Distance)</th>
<th>Max. Input Power</th>
<th>Input Sensitivity</th>
<th>Input Wavelength</th>
<th>Output Power</th>
<th>Output Wavelength</th>
<th>Loop Back</th>
</tr>
</thead>
<tbody>
<tr>
<td>G0332A 100M FX 1310 nm MM SFP</td>
<td>100BASE-FX 1310 nm multi mode (2 km)</td>
<td>-14 dBm</td>
<td>-31 dBm</td>
<td>1270 nm to 1600 nm</td>
<td>-20 to -15 dBm</td>
<td>1280 nm to 1380 nm</td>
<td>OK</td>
</tr>
<tr>
<td>G0329A 10G LR 1310 nm SFP+</td>
<td>10GBASE-LR 1310 nm single mode (10 km)</td>
<td>+0.5 dBm</td>
<td>-14 dBm</td>
<td>1260 nm to 1355 nm</td>
<td>-8.2 to +0.5 dBm</td>
<td>1260 nm to 1355 nm</td>
<td>OK</td>
</tr>
<tr>
<td>G0315A 10G LR/LW 1310 nm SFP+</td>
<td>10GBASE-LR 1310 nm single mode (10 km)</td>
<td>+0.5 dBm</td>
<td>-14.4 dBm</td>
<td>1260 nm to 1565 nm</td>
<td>-6 to -1 dBm</td>
<td>1290 nm to 1330 nm</td>
<td>OK</td>
</tr>
<tr>
<td>G0316A 10G LR/LW 1310 nm SFP+</td>
<td>10GBASE-LR 1550 nm single mode (40 km)</td>
<td>-1 dBm</td>
<td>-15.8 dBm</td>
<td>1260 nm to 1565 nm</td>
<td>-3 to +3 dBm</td>
<td>1530 nm to 1565 nm</td>
<td>&gt;4 dB ATT</td>
</tr>
<tr>
<td>G0318A 100G BASE-LR4 QSFP28</td>
<td>100GBASE-LR 1310 nm single mode (80 km)</td>
<td>-8 dBm</td>
<td>-22 dBm</td>
<td>1260 nm to 1565 nm</td>
<td>0 to +5 dBm</td>
<td>1525 nm to 1565 nm</td>
<td>&gt;13 dB ATT</td>
</tr>
<tr>
<td>G0319A 10G LR/LW 1310 nm SFP+</td>
<td>10GBASE-LR 1310 nm single mode (80 km)</td>
<td>-8 dBm</td>
<td>-18 dBm</td>
<td>1270 nm to 1580 nm</td>
<td>-5 to 0 dBm</td>
<td>1260 nm to 1360 nm</td>
<td>OK</td>
</tr>
<tr>
<td>G0320A 10G LR/LW 1310 nm SFP+</td>
<td>10GBASE-LR 1310 nm single mode (80 km)</td>
<td>-9 dBm</td>
<td>-27 dBm</td>
<td>1270 nm to 1580 nm</td>
<td>-2 to +3 dBm</td>
<td>1280 nm to 1335 nm</td>
<td>&gt;12 dB ATT</td>
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<tr>
<td>G0321A 10G LR/LW 1310 nm SFP+</td>
<td>10GBASE-LR 1310 nm single mode (80 km)</td>
<td>-9 dBm</td>
<td>-28 dBm</td>
<td>1270 nm to 1580 nm</td>
<td>-2 to +3 dBm</td>
<td>1500 nm to 1580 nm</td>
<td>&gt;12 dB ATT</td>
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<tr>
<td>G0322A 1G/2G/4G FC 1310 nm SFP</td>
<td>1GFC, 2GFC, 4GFC 1310 nm single mode (0.5 km)</td>
<td>-3 dBm</td>
<td>-15 dBm</td>
<td>830 nm to 860 nm</td>
<td>-9 to 0 dBm</td>
<td>830 nm to 860 nm</td>
<td>&gt;3 dB ATT</td>
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<tr>
<td>G0323A 1G/2G/4G FC 1310 nm SFP</td>
<td>1GFC, 2GFC, 4GFC 1310 nm single mode (0.5 km)</td>
<td>-3 dBm</td>
<td>-18 dBm</td>
<td>1260 nm to 1360 nm</td>
<td>-8 to 0 dBm</td>
<td>1260 nm to 1360 nm</td>
<td>&gt;3 dB ATT</td>
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<tr>
<td>G0356A 10GBASE-SR 1550 nm SFP+</td>
<td>10GBASE-SR 1550 nm multi mode (0.3 km)</td>
<td>-1 dBm</td>
<td>-11.1 dBm</td>
<td>840 nm to 860 nm</td>
<td>-7.3 to -1.0 dBm</td>
<td>840 nm to 860 nm</td>
<td>OK</td>
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<tr>
<td>G0359A 40G SFP+ 850 nm SFP+</td>
<td>40GBASE-SR4 850 nm multi mode (0.1 km)</td>
<td>+2.4 dBm (per Lane)</td>
<td>-9.9 dBm</td>
<td>840 nm to 860 nm</td>
<td>-8 to +2.4 dBm</td>
<td>840 nm to 860 nm</td>
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<tr>
<td>G0344A 10GBASE-40X 1310 nm SFP+</td>
<td>40G Ethernet/OTN 1310 nm single mode (10 km)</td>
<td>+2.3 dBm (per Lane)</td>
<td>-11.5 dBm (per Lane)</td>
<td>1264.5 nm to 1277.5 nm</td>
<td>-2 to +2.3 dBm (per Lane)</td>
<td>1264.5 nm to 1277.5 nm</td>
<td>OK</td>
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<td>G0335A 40G SFP+ 100G SFP28</td>
<td>40G Ethernet/OTN 1310 nm single mode (10 km)</td>
<td>+2.3 dBm (per Lane)</td>
<td>-11.1 dBm (per Lane)</td>
<td>1264.5 nm to 1277.5 nm</td>
<td>-2 to +2.3 dBm (per Lane)</td>
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<td>40G SFP+ 1550 nm SFP+</td>
<td>+3 dBm (per Lane)</td>
<td>-6 dBm</td>
<td>1530 nm to 1565 nm</td>
<td>0 to +3 dBm</td>
<td>1530 nm to 1565 nm</td>
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<td>G0337A 100G LR 1310 nm SFP+</td>
<td>100G Ethernet/OTN 1310 nm single mode (10 km)</td>
<td>+4.5 dBm (per Lane)</td>
<td>-10.3 dBm (per Lane)</td>
<td>1294.53 nm to 1296.59 nm</td>
<td>+8.9 dBm (max) (Total)</td>
<td>1294.53 nm to 1296.59 nm</td>
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<td>G0338A 100G LR 1310 nm SFP+</td>
<td>100G Ethernet/OTN 1310 nm single mode (10 km)</td>
<td>+4.5 dBm (per Lane)</td>
<td>-10.3 dBm (per Lane)</td>
<td>1294.53 nm to 1296.59 nm</td>
<td>+8.9 dBm (max) (Total)</td>
<td>1294.53 nm to 1296.59 nm</td>
<td>OK</td>
</tr>
<tr>
<td>G0339A 100G SFP+ 850 nm SFP+</td>
<td>100G Ethernet 850 nm multi mode (0.1 km)</td>
<td>+2.4 dBm (per Lane)</td>
<td>-9.5 dBm (per Lane)</td>
<td>840 nm to 860 nm</td>
<td>+8.9 dBm (max) (Total)</td>
<td>840 nm to 860 nm</td>
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<tr>
<td>G0364A 10GBASE-SR4 850 nm SFP+</td>
<td>10GBASE-SR4 850 nm single mode (0.1 km)</td>
<td>+2.4 dBm (per Lane)</td>
<td>-9.9 dBm (per Lane)</td>
<td>840 nm to 860 nm</td>
<td>+8.9 dBm (max) (Total)</td>
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<tr>
<td>G0365A 100G LWR 1310 nm SFP+</td>
<td>100G Ethernet/OTN 1310 nm single mode (10 km)</td>
<td>+4.5 dBm (per Lane)</td>
<td>-8.6 dBm (per Lane)</td>
<td>1294.53 nm to 1296.59 nm</td>
<td>+10.5 dBm (max) (Total)</td>
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<tr>
<td>G0366A 100G SFP+ 850 nm SFP+</td>
<td>100G Ethernet/OTN 1310 nm single mode (10 km)</td>
<td>+4.5 dBm (per Lane)</td>
<td>-8.6 dBm (per Lane)</td>
<td>1294.53 nm to 1296.59 nm</td>
<td>+10.5 dBm (max) (Total)</td>
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<td>622 M Gig STM-4</td>
<td>1 G Gig FC</td>
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<td>100G LR4 Dual Rate 1310 nm QSFP28</td>
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<tr>
<td>G0369A</td>
<td>100G LR4 Dual Rate 1310 nm CFP4</td>
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</tr>
</tbody>
</table>
### Network Master Flex MT1100A Mainframe

#### User Interfaces
- **Display**: 12.1-inch active matrix TFT display (800 × 600 pixels) and touch screen
- **Supported Languages**: English, Chinese, Japanese, French, Russian, Spanish

#### Service Interfaces
- **USB Interface**: MT1100A operates as host: USB 2.0 type A (2 ports)
  MT1100A operates as device: USB 2.0 type Mini-B (1 port)
- **Ethernet Interface**: Ethernet 10M/100M/1000M, Connector: RJ45
- **WLAN Interface**: IEEE 802.11 b/g/n
- **Bluetooth Interface**: Bluetooth 2.1 + EDR
  ∗ Available for certified countries and regions including USA, Canada, Japan and EU countries. Please visit the Anritsu web site for updated information.

#### Other Interfaces
- **Unit Synchronization Input**: (Not used)
- **Unit Synchronization Output**: (Not used)
- **Audio Interface**: For connection of CTIA Standard head set
  Connector: 3.5-mm diameter jack
- **AUX Connector**: For connection of G0325A GPS receiver
- **Built-in Loudspeaker**: Monitors speech of voice channel
  Output level: user-controlled from user Interface
- **Ext. Clock Input**: For connection of external clock signals:
  - SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps), or 2.048 MHz TTL signal in accordance with ITU-T G.703, 10 MHz
  Connector: BNC

#### Miscellaneous
- **Dimensions and Mass**: 320 (W) × 225 (H) × 46 (D) mm (excluding projections), ≤2.5 kg
- **Environmental**
  - Operating: 0° to +40°C, ≤80% RH (non-condensing)
  - Storage: -20° to +60°C, ≤80% RH (non-condensing)

#### Battery and AC Power Supply Module MU110001A
- **Battery**: 14.4 V rechargeable and replaceable intelligent Li-ion battery
  Operation time: 1 hour (typ.) (with MU110011A, 100 Gbps Ethernet operation)
  Charging time: 6 hours (typ.) (25°C)
  Remaining capacity indication:%
- **Power Supply**: 100 V(ac) to 240 V(ac), 50 Hz/60 Hz
  380 VA (max.)
- **Dimensions and Mass**: 320 (W) × 225 (H) × 82 (D) mm (excluding projections), ≤3.0 kg (without battery)
- **Environmental**
  - Operating: 0° to +40°C, ≤80% RH (non-condensing)
  - Storage: -20° to +60°C, ≤80% RH (non-condensing), with battery
  - -20° to +50°C, ≤80% RH (non-condensing, without battery)
- **Module Combination**: 1 module: Un limited
  2 modules: MU110010A + MU110010A
  MU110010A + MU110011A
  MU110010A + MU110013A

#### CE EMC
- EN61326-1, EN61000-3-2
- EN61010-1
- EN50581

### AC only High Power Supply Module MU110002A
- **Power Supply**: 100 V(ac) to 240 V(ac), 50 Hz/60 Hz
  700 VA (max.)
- **Dimensions and Mass**: 320 (W) × 225 (H) × 72 (D) mm (excluding projections), ≤3.0 kg
- **Environmental**
  - Operating: 0° to +40°C, ≤80% RH (non-condensing)
  - Storage: -20° to +60°C, ≤80% RH (non-condensing)

#### CE EMC
- EN61326-1, EN61000-3-2
- EN61010-1
- EN50581
Measurement Modules Specifications

10G Multirate Module MU110010A

Test Port/Reference Standard
- SFF-8431, SFF-8472 compliant, IEEE 802.3ae-2002, IEEE 802.3-2008 compliant
- 10Gbps: 2 ports
- IEEE 802.3-2008 10BASE-T, 100BASE-TX, 1000BASE-T compliant
- Auto MDI-X
- 10 Mbps/100 Mbps full/half duplex, 1000 Mbps full duplex
- BNC: 2 ports
- ITU-T G.703 compliant
- RJ48: 2 ports
- ITU-T G.703 compliant
- RTT Bantam: 2 ports
- ANSI DS1.102 compliant

Bit Rate

<table>
<thead>
<tr>
<th>Standard</th>
<th>Bit Rate</th>
<th>Interfaces</th>
</tr>
</thead>
<tbody>
<tr>
<td>10BASE-T</td>
<td>12.5 Mbps</td>
<td>RJ45</td>
</tr>
<tr>
<td>100BASE-T</td>
<td>125 Mbps</td>
<td>RJ45</td>
</tr>
<tr>
<td>100BASE-XX</td>
<td>125 Mbps</td>
<td>SFP</td>
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<tr>
<td>1000BASE-XX</td>
<td>125 Mbps</td>
<td>SFP</td>
</tr>
<tr>
<td>10GBASE-XX</td>
<td>10.3125 Gbps</td>
<td>SFP+</td>
</tr>
<tr>
<td>STM-1/OC-3</td>
<td>155.52 Mbps</td>
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</tr>
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<td>STM-4/OC-12</td>
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<td>SFP</td>
</tr>
<tr>
<td>STM-16/OC-48</td>
<td>2.48832 Gbps</td>
<td>SFP</td>
</tr>
<tr>
<td>STM-64/OC-192</td>
<td>9.95328 Gbps</td>
<td>SFP+</td>
</tr>
<tr>
<td>OTU1</td>
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<td>10.70922532 Gbps</td>
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<td>11.04910714 Gbps</td>
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<td>SFP+</td>
</tr>
<tr>
<td>OTU1f</td>
<td>11.27008929 Gbps</td>
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<tr>
<td>OTU2f</td>
<td>11.3764241 Gbps</td>
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<td>1GFC</td>
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<td>SFP</td>
</tr>
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<tr>
<td>10GFC</td>
<td>10.51875 Gbps</td>
<td>SFP+</td>
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</table>

Tx Ref. Clock Output
- Frequency: Selectable from 1/16, or 1/64 against the bit rate. (Available only when one of SFP ports is selected)
- Level: 250 mVp-p (min.), 550 mVp-p (max.)
- Termination: 50Ω/AC (Single ended)
- Connector: SMA

Dimensions and Mass
- 320 (W) × 225 (H) × 37 (D) mm, ≤1.4 kg

Environmental
- Temperature and Humidity
  - Operating: 0°C to +40°C, ≤80% RH (non-condensing)
  - Storage: -20°C to +60°C, ≤80% RH (non-condensing)

CE
- EN61326-1, EN61000-3-2
- LVD EN61010-1
- RoHS EN50581

Laser Safety
- IEC 60825-1: 2007 CLASS 1
- 21CFR1040.10 and 1040.11*2
- 21CFR1040.10 and 1040.11*

Safety measures for laser products
- This product complies with optical safety standards in 21CFR1040.10, 1040.11 and IEC 60825-1; the following descriptive labels are affixed to the product.

*1: The frequency accuracy depends on the accuracy of the MT1100A internal clock or the external clock of MT1100A.
*2: Excludes deviations caused by conformance to Laser Notice No. 50 dated June 24, 2007
*3: Safety measures for laser products

This product complies with optical safety standards in 21CFR1040.10, 1040.11 and IEC 60825-1; the following descriptive labels are affixed to the product.
### 100G Multirate Module MU110011A

Test Port/Reference Standard
- CFP: 1 port
- CFP MSA Hardware Specification, Rev. 1.4 compliant
- CFP MSA Management Interface Specification V2.2 R06a compliant (Not supported to MSA 100GLH)
- IEEE 802.3ba-2010 compliant
- SFP/SFP+: 2 ports
- SFF-8431, SFF-8472 compliant
- IEEE 802.3-2008 compliant
- RJ45: 2 ports
- IEEE 802.3-2008 10BASE-T, 100BASE-TX, 1000BASE-T compliant
- Auto MDI-X
- 10 Mbps/100 Mbps full/half duplex, 1000 Mbps full duplex

**Bit Rate**

<table>
<thead>
<tr>
<th>Standard</th>
<th>Bit Rate</th>
<th>Interfaces</th>
</tr>
</thead>
<tbody>
<tr>
<td>10BASE-T</td>
<td>12.5 Mbps</td>
<td>RJ45</td>
</tr>
<tr>
<td>100BASE-T</td>
<td>125.0 Mbps</td>
<td>RJ45</td>
</tr>
<tr>
<td>100BASE-XX</td>
<td>125.0 Mbps</td>
<td>SFP+</td>
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<td>100BASE-X/2</td>
<td>1.25 Gbps</td>
<td>SFP</td>
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<td>10.3125 Gbps</td>
<td>SFP+</td>
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<td>10G GigE</td>
<td>10.3125 Gbps × 4 Lane</td>
<td>CFP, QSFP+</td>
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<tr>
<td>40G GigE</td>
<td>41.25 Gbps</td>
<td>SFP+</td>
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<tr>
<td>100 GigE</td>
<td>10.155 Gbps × 10 Lane</td>
<td>SFP</td>
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<tr>
<td>ST-10G-8C</td>
<td>1.25 Gbps</td>
<td>SFP</td>
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<td>ST-4/8G-12</td>
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<td>SFP</td>
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<td>SFP</td>
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<td>ST-64G-128</td>
<td>9.93528 Gbps</td>
<td>SFP+</td>
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<td>ST-256G-128</td>
<td>9.93528 Gbps × 4 Lane</td>
<td>CFP, QSFP+</td>
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<td>FR model</td>
<td>39.81321 Gbps</td>
<td>CFP</td>
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<td>OTU2</td>
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<td>FR model</td>
<td>43.01841356 Gbps</td>
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</table>

**Tx Ref. Clock Output**
- Frequency: Select 1/16 or 1/64 for bit rates of 10G or less. Select 1/16 or 1/64 for each lane rate for XLAUI and OTL3.4 of 40G. Select 1/16 or 1/64 for each lane rate for CAUI and OTL4.19 of 100G. (RJ45 port cannot be selected)
- Level: 250 mVpp-p (min.), 550 mVpp-p (max.)
- Termination: 50Ω/AC (Single ended)
- Connector: SMA

**Dimensions and Mass**
- 320 (W) × 225 (H) × 60 (D) mm, ≤3.0 kg

**Environmental**
- Temperature and Humidity
  - Operating: 0° to +40°C, ≤80% RH (non-condensing)
  - Storage: -20° to +60°C, ≤80% RH (non-condensing)
- CE
  - EMC: EN61326-1, EN61000-3-2
  - LVD: EN61010-1
  - RoHS: EN50581

**Laser Safety**
- IEC 60825-1: 2007 CLASS 1
  - 21CFR1040.10 and 1040.11
    - CFP: 100GBASE-LR4, 40GBASE-LR4, 40GBASE-FR
    - QSFP+: 40GBASE-LR4
    - SFP: 4GFC(SX), 4GFC(LX), 4GFC(EX), OC-48 LR-1/STM L-16.1, OC-48 LR-2/STM L-16.2, 100BASE-FX, 100BASE-LX
    - SFP+: 1000BASE-SX/LX/ZX, 1000BASE-LR, 1000BASE-ER, 1000BASE-ZR
  - IEC 60825-1: 2007 CLASS 1M
    - 21CFR1040.10 and 1040.11
    - CFP: 100GBASE-SR10
    - QSFP+: 40GBASE-SR4

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*1: The frequency accuracy depends on the accuracy of the MT1100A internal clock or the external clock of MT1100A. Refer to the external interfaces in MT1100A specifications.
*2: Excludes deviations caused by conformance to Laser Notice No. 50 dated June 24, 2007
*3: Safety measures for laser products
This product complies with optical safety standards in 21CFR1040.10, 1040.11 and IEC 60825-1; the following descriptive labels are affixed to the product.
### Measurement Modules Specifications

#### 40/100G Advanced Module MU110013A

**Test Port/Reference Standard**
- CFP2: 2 ports
  - CFP MSA CFP2 Hardware Specification, Rev. 1.0 compliant
  - CFP MSA Management Interface Specification V2.2 R06a compliant (Not supported to MSA 100GLH)
  - IEEE 802.3ba-2010 compliant
- CXP: 2 ports
  - InfiniBand Architecture 1.2.1 Annex A6: CXP compliant
  - SFF-8642, IEEE 802.3ba-2010 compliant
- QSFP+: 2 ports
  - SFF-8436, SFF-8472 compliant
  - IEEE 802.3ba-2010 compliant

**Bit Rate**

<table>
<thead>
<tr>
<th>Standard</th>
<th>Bit Rate</th>
<th>Interfaces</th>
</tr>
</thead>
<tbody>
<tr>
<td>40 GigE</td>
<td>10.3125 Gbps × 4 Lane</td>
<td>QSFP+</td>
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<tr>
<td>100 GigE</td>
<td>10.3125 Gbps × 10 Lane</td>
<td>CFP</td>
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<tr>
<td>100 GigE</td>
<td>25.78125 Gbps × 4 Lane</td>
<td>CFP2, CFP4**, QSFP28**</td>
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<td>OTU3</td>
<td>10.75460339 Gbps × 4 Lane</td>
<td>QSFP+</td>
</tr>
<tr>
<td>OTU3e1</td>
<td>11.14274364 Gbps × 4 Lane</td>
<td>QSFP+</td>
</tr>
<tr>
<td>OTU3e2</td>
<td>11.14583889 Gbps × 4 Lane</td>
<td>QSFP+</td>
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<td>OTU4</td>
<td>27.952493392 Gbps × 4 Lane</td>
<td>CFP2, CFP4**, QSFP28**</td>
</tr>
</tbody>
</table>

**Tx Ref. Clock Output**
- Frequency
  - Select 1/16 or 1/64 for each lane rate of XX.
  - 40 GigE: XLAUI
  - OTU3, OTU3e1, OTU3e2: OTL3.4
  - 100 GigE: CAUI
  - OTU4: OTL4.10
- Level: 250 mVp-p (min.), 550 mVp-p (max.)
- Termination: 50Ω/AC (Single ended)
- Connector: SMA

**Sync Clock Output**
- Frequency
  - Select 1/8 or 1/16 against the bit rate of the data lane for CFP2 port.
  - 100 GigE: CAUI4
  - OTU4: OTL 4.4
- Level: 150 mVp-p (min.), 650 mVp-p (max.)
- Termination: 50Ω/AC (Single ended)
- Connector: SMA

**Dimensions and Mass**
- 320 (W) × 225 (H) × 60 (D) mm, ≤3.0 kg

**Environmental**
- Temperature and Humidity
  - Operating: 0° to +40°C, ≤80% RH (non-condensing)
  - Storage: −20° to +60°C, ≤80% RH (non-condensing)

**CE**
- EMC: EN61326-1, EN61000-3-2
- LVD: EN61010-1
- RoHS: EN50581

**Laser Safety**
- IEC 60825-1: 2007 CLASS 1
  - 21CFR1040.10 and 1040.11**
  - QSFP+: 40G BASE-LR4
  - CFP2: 100G BASE-LR4
  - CFP4: 100G BASE-LR4
  - QSFP28: 100G BASE-LR4
- IEC 60825-1: 2007 CLASS 1M
  - 21CFR1040.10 and 1040.11**
  - QSFP+: 40G BASE-SR4
  - CFP: 100G BASE-SR10
  - QSFP28: 100G BASE-SR4

**Notes**
- **1:** The frequency accuracy depends on the accuracy of the MT1100A internal clock or the external clock of MT1100A. Refer to the external interfaces in MT1100A specifications.
- **2:** Required for an interface converter.
- **3:** Excludes deviations caused by conformance to Laser Notice No. 50 dated June 24, 2007
- **4:** Safety measures for laser products

This product complies with optical safety standards in 21CFR1040.10, 1040.11 and IEC 60825-1; the following descriptive labels are affixed to the product.
OTN Testing Specifications

OTN Testing

Framing

- OTU4, OTU3, OTU3e1, OTU3e2, OTU2, OTU2e, OTU1e, OTU2f, OTU1f, OTU1

Transmitter Clock

- Internal clock accuracy: ±4.6 ppm, Clock offset: ±200 ppm (0.1 ppm steps)
- Received clock
- TTL level external 2 MHz clock
- SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
- Signal from optional GPS receiver

Receive Signal Rate

- ±200 ppm
- Frequency deviation indication resolution: ±0.1 ppm

Scrambling

- Complies with ITU-T G.709

OTN Mapping

See page 5, 6

OTN Alarms

- Detected alarms
  - OTU layer: OTU-AIS, LOF, OOF, LOM, SM-TIM, SM-BIAE, SM-BDI, SM-IAE
  - ODU layer: LOS, ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSD
  - ODU multiplexing: ODU-LOFLOM, ODU-OOF, OOM, ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, MSIM
  - OPU layer: PLM, OPU-MSIM, Client-AIS, CSF, LSS
  - User-defined TIM, TCMI-BIAE, TCMI-IAE, TCMI-TCMI-TIM, TCMI-LTC (i = 1 to 6)
  - OTL: LOF, OOF, OOR, LOR, ILA/OLA (OTU4, OTU3, OTU3e1, OTU3e2)
- Generated alarms
  - OTU layer: OTU-AIS, OTU-OOF/FLOF, OOM/LOM, SM-TIM, SM-BIAE, SM-BDI, SM-IAE
  - ODU layer: ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSD
  - ODU multiplexing: OOF/LOF, OOM/LOM, ODU-AIS, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSD
  - OPU layer: Client-AIS, CSF
  - TCMI-TIM, TCMI-BIAE, TCMI-IAE, TCMI-TCMI-TIM, TCMI-LTC (i = 1 to 6)
  - OTL: LOF, OOF, OOR, LOR (OTU4, OTU3, OTU3e1, OTU3e2)

OTN Errors

- Detected errors
  - OTU layer: FAS, MFAS, SM-BEI, SM-BIP8, FEC-Correctable, FEC-Uncorrectable
  - ODU layer: PM-BIP8, PM-BEI
  - OPU layer: Pattern error
  - GMP error: CRC8 error, CRC5 error
  - GFP errors: cHEC corrected, cHEC uncorrectable, tHEC corrected, tHEC uncorrectable, CSF Signal, CSF Sync, Invalid GFP Frame, Superblock CRC, eHEC corrected, eHEC uncorrectable, FCS, CMF Sync, SSF, PTI Missmatch, UPI Mismatch
  - TCMI: TCMI-BEI, TCMI-BIP8 (i = 1 to 6)
  - OTL: MFAS, LLM (OTU4, OTU3, OTU3e1, OTU3e2)
- Generated errors
  - OTU layer: Bit all, FAS, OTU-FAS, MFAS, SM-BIP8, SM-BEI
  - ODU layer: PM-BIP8, PM-BEI, OTU-FAS
  - TCMI-BIP8, TCMI-BEI (i = 1 to 6)
  - Pattern error
  - OTL: MFAS, LLM (OTU4, OTU3, OTU3e1, OTU3e2)
  - GMP: CRC8, CRC5, Invalid JC1, Invalid JC2, Invalid JC1&JC2
  - GFP: cHEC, tHEC, Superblock CRC, eHEC, FCS, CMF
- Inserted Error bits are editable

Error Performance

- G.8201/M.2401 analysis of received signal based on detected errors and alarms: BBE, BBER, SES, SESR, UNAV

Justification Analysis

- Count
- AMP: Positive (+1), Positive (+2), Negative (-1), Offset (ppm)
- GMP: CRC8 Error, CRC5 Error, Inc, Inc > 1, Inc > 2, Inc Over, Dec, Dec > 1, Dec > 2, Dec Over, Offset (ppm), Cm (t) Max., Cm (t) Min.

BER Test Pattern

- Pattern generation and detection for bulk test patterns:
  - Test patterns: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, Null
  - PRBS patterns can be inverted
  - User-defined patterns (Pattern length: 2048 bits, 32 bits)

FEC Test

- ITU-T O.182 Random error insertion

Overhead

- User-editable header bytes
  - OTU layer: FAS, SM, GCC0, RES
  - ODU layer: PM, FTFL, APS/PCC, GCC1, GCC2, RES, EXP, TCMI (i = 1 to 6)
  - OPU layer(s): PSI
- Capture and display current overhead bytes
- The following signals are decoded: TTI (SM, PM, TCMI (i = 1 to 6) of high-order, FTFL, PT)

OTL Skew

- OTU4, OTU3, OTU3e1, OTU3e2
- Insertion
  - Bits: 0 to 32000 (Tx lane)
- Detection
  - Relative skew, Marker map

Through Mode

- Transparent mode
- OH overwrite mode
- The OTU, ODU and OPU overhead can be changed
- The FEC encoder and decoder can be set On/Off in any mode
# OTN/Ethernet Testing Specifications

## OTN Results

<table>
<thead>
<tr>
<th>Status</th>
<th>Current information on:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>• Alarms and errors on monitored line</td>
</tr>
<tr>
<td></td>
<td>• Input level indication for optical signals</td>
</tr>
<tr>
<td></td>
<td>• Frequency</td>
</tr>
<tr>
<td></td>
<td>• Frequency deviation</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Statistics</th>
<th>User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Logged information: Alarms (s), Errors (count or count and ratio), Client Frequency, Deviation</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>APS</th>
<th>APS (Automatic Protection Switching) test and analysis</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>• APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted.</td>
</tr>
<tr>
<td></td>
<td>• Start and stop triggers can be selected independently.</td>
</tr>
<tr>
<td></td>
<td>• Trigger events can be selected from the high-order OTU and ODU, Pattern bit error, LOS (Loss of Signal).</td>
</tr>
<tr>
<td></td>
<td>• Switching time, Switching count, Pass/Fail, Minimum, Maximum and Average can be displayed.</td>
</tr>
<tr>
<td></td>
<td>• APS switching time measurement resolution: 0.1 ms</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Round Trip Delay (Propagation Time) Measurement</th>
<th>Resolution: 0.1 µs Measured Max. time: 10.0 s Interval: 0.5, 1, 2, 5, 10 s</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>Tributary Scan</th>
<th>Supports up to 10 Gbps</th>
</tr>
</thead>
</table>

## Ethernet Testing

<table>
<thead>
<tr>
<th>Ethernet Test</th>
<th>Monitor/Generate, Pass-through, Reflector</th>
</tr>
</thead>
<tbody>
<tr>
<td>Encapsulation</td>
<td>• EtherType II (DIX v.2), IEEE 802.3 with 802.2 (LLC1), IEEE 802.3 with SNAP</td>
</tr>
</tbody>
</table>

### 100 GigE FEC

<table>
<thead>
<tr>
<th>Interface</th>
<th>: CFP2 slots</th>
</tr>
</thead>
<tbody>
<tr>
<td>FEC enable</td>
<td>On/OFF</td>
</tr>
<tr>
<td>FEC Code</td>
<td>RS (528, 514, 7, 10)</td>
</tr>
<tr>
<td>FEC status &amp; counter</td>
<td>Loss of FEC alignment, Corrected CW, Uncorrectable CW, Corrected Symbol each lane</td>
</tr>
<tr>
<td>FEC related Error Injection</td>
<td>Bit error per CAUI4 lane encoded RS-FEC (Injection timing is Single/Burst.)</td>
</tr>
</tbody>
</table>

### Configuration, Monitor/Generate

<table>
<thead>
<tr>
<th>Traffic Generation</th>
<th>Variable line rate traffic generation, up to full line rate</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Line load profile: Constant, Ramp</td>
</tr>
<tr>
<td></td>
<td>Traffic duration: Continuous, Programmable number of seconds or frames</td>
</tr>
<tr>
<td></td>
<td>Adjusted frame size: 60 bytes to 16000 bytes</td>
</tr>
<tr>
<td></td>
<td>Frame sizes: Constant, Stepped, Random</td>
</tr>
<tr>
<td></td>
<td>Payload profiles: Data, Video, Voice</td>
</tr>
<tr>
<td></td>
<td>User-defined traffic mix of unicast and broadcast frames</td>
</tr>
<tr>
<td></td>
<td>Fixed or incremented IP identifier</td>
</tr>
<tr>
<td></td>
<td>User programmable DSCP/TOS byte</td>
</tr>
<tr>
<td></td>
<td>Configurable IP and Ethernet source and destination addresses (supports IPv4 and IPv6 addressing)</td>
</tr>
<tr>
<td></td>
<td>IPv4: Fixed, DHCP, DNS</td>
</tr>
<tr>
<td></td>
<td>IPv6: Fixed</td>
</tr>
<tr>
<td></td>
<td>• Address increment, Decrement and Random generation supported</td>
</tr>
<tr>
<td></td>
<td>• User programmable UDP/TCP address</td>
</tr>
<tr>
<td></td>
<td>• Automatic TCP connect (user selectable)</td>
</tr>
<tr>
<td></td>
<td>• UDP check sum: Automatic, Fixed (null); TCP check sum: Automatic</td>
</tr>
<tr>
<td></td>
<td>• Generate pause frames, Respond to pause frames</td>
</tr>
<tr>
<td></td>
<td>• Answer incoming ARP, Ping requests (On/Off)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Stacked VLAN</th>
<th>Up to 8 user-settable VLAN tags</th>
</tr>
</thead>
<tbody>
<tr>
<td>Parameters per VLAN tag:</td>
<td>- Ether-type 0x8100 (802.1Q), 0x88a8 (802.1ad), 0x9100 or 0x9200</td>
</tr>
<tr>
<td></td>
<td>• User-defined VLAN ID, CFI, VLAN priority</td>
</tr>
<tr>
<td></td>
<td>• Address increment, Decrement and Random generation supported</td>
</tr>
<tr>
<td></td>
<td>Only one VLAN level supported at ping, traceroute and RFC 2544 router latency tests</td>
</tr>
</tbody>
</table>

### Multistream

<table>
<thead>
<tr>
<th>Multistream</th>
<th>Number of streams: Up to 16 streams per port can be activated</th>
</tr>
</thead>
</table>

### Timing Functionality

<table>
<thead>
<tr>
<th>Timing Functionality</th>
<th>Timing sources (selectable): Internal, Received clock, 2-MHz signal, SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps), PTP (IEEE 1588 v2) recovered clock or signal from optional GPS receiver</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Frequency deviation: ±200 ppm (0.1 ppm steps) The frequency deviation of received Ethernet signals can be measured against the internal clock.</td>
</tr>
</tbody>
</table>

### Receiver Setting

<table>
<thead>
<tr>
<th>Receiver Setting</th>
<th>User-defined expected preamble length: 3 bytes to 15 bytes</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>User-defined IFG lower threshold: 8 bytes to 15 bytes (Ethernet 10 Mbps, 100 Mbps, 1000 Mbps)</td>
</tr>
<tr>
<td></td>
<td>User-defined jumbo frame size upper limit: 1519 bytes to 16000 bytes</td>
</tr>
</tbody>
</table>

### Error Generation

<table>
<thead>
<tr>
<th>Error Generation</th>
<th>FCS, Preamble, Error symbol, IFGs for ethernet 10 Mbps, 100 Mbps, 1000 Mbps, Wrong IP check sum, Fragmented IP, Wrong layer 4 check sum, PRBS bit error, BER test sequence error</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>40 Gbps/100 Gbps: Invalid block type (0x00, 0x2d, 0x33, 0x66), Invalid sync. header (00, 11), Invalid alignment marker, BIP error</td>
</tr>
</tbody>
</table>

### Alarm Generation

<table>
<thead>
<tr>
<th>Alarm Generation</th>
<th>No link, Remote fault, Local fault (10 Gbps)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>PCS 10 Gbps/40 Gbps/100 Gbps: High BER</td>
</tr>
</tbody>
</table>

### PCS Skew

<table>
<thead>
<tr>
<th>PCS Skew</th>
<th>40 Gbps, 100 Gbps</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Insertion: 100 Gbps Tx lane: 0 to 4224 bits</td>
</tr>
<tr>
<td></td>
<td>40 Gbps, 100 Gbps physical lane: 0 to 8448 bits</td>
</tr>
<tr>
<td></td>
<td>Detection: Relative skew, Marker map</td>
</tr>
</tbody>
</table>
**Adjustable Thresholds**

Utilization, Throughput and Errored frames

Signal level indication for optical Ethernet interfaces

Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.

**Resolution**

User-defined resolution for statistical measurements: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h

**Frame Statistics**

- Total frames, Total valid frames, Unicast/Multicast/Broadcast frames, Number of pause frames
- Number of VLAN tagged frames, Max. number of VLAN layers detected, Last received VLAN ID, Last received VLAN priority
- Number of MPLS frames and MPLS-TP frames. Max. number of MPLS layers detected. Last received MPLS Label, MPLS Priority and MPLS TTL.
- Number of PBB frames. Last received B/I-tag ID and B/I-tag priority.
- Total errored frames, Fragmented frames, Number of oversized and undersized (runts) frames, Number of FCS errored frames, Error symbol frames (not Ethernet 10 Gbps)/Code violation frames (Ethernet 10 Gbps), Number of collisions (10 Mbps, 100 Mbps half duplex), Preamble violations, IGF violations (Ethernet 10 Mbps, 100 Mbps, 1000 Mbps), False carrier, 10G LFS RF (local fault), 10G LFS RF (remote fault)

**Burst Statistics**

Total frames, Total valid frames, Number of burst, Total frames in bursts, Burst size (Max./Min./Avg.)

**Frame Distribution Statistics**

- Total valid/ frames, 64 to 127, 128 to 255, 256 to 511, 512 to 1023, 1024 to 1518 byte frames, Total number of jumbo frames
- Frame size (Max./Min./Avg.)

**Multistream Statistics**

Available information per stream:

- Frame loss count/ rate, Throughput, Latency, Packet jitter, Frames and bytes received and transmitted

**Transmit Statistics**

Total frames, Total valid frames, Unicast/Multicast/Broadcast frames, FCS errors, Total errors

**Filter**

Up to 8 filter conditions can be defined.

Each condition can filter using:

- IP or MAC source address, IP or MAC destination address, Broadcast address, IEEE OUI value, Encapsulation type, VLAN ID and VLAN tag priority, MPLS, PBB source and destination MAC address, PBB B/I-tag, MPLS-TP source and destination MAC address, PBB B/I-tag, MPLS-TP source and destination MAC address, ITU-T Y.1564 Service Activation Test

**BER Test and Service Disruption Measurement**

Generation and detection of test patterns, Count of errors in received test pattern, Pattern generation: Unframed (Layer 1), Framed Ethernet (MAC) header (Layer 2), Framed Ethernet (MAC) header with IP header (Layer 3) or Framed Ethernet (MAC) header, Framed with IP header and TCP/UDP header (Layer 4), User-defined header pattern (14 byte to 256 byte)

Detection of sequence errors and loss of sequence synchronization

Frame loss count and frame loss seconds

Throughput measurement results are calculated for:

- Utilization layer, Physical layer, Physical layer excluding preamble, Link layer, Network layer and Data layer
- Min./Max./Avg. values

Performance (M.2100 type) parameters: ES, SES, ALS, UAT, AVT, EFS

Test patterns: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, HF test pattern, CRPAT, JTPAT, SPAT, 55 Hex, FOX, 32-bit user programmable

User-defined resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h

**Error Generation**

FCS, Preamble, Error symbol, IGF for Electrical Interface (10 Mbps, 100 Mbps, 1000 Mbps), Wrong IP check sum, Fragmented IP, Wrong layer 4 check sum, PRBS bit error, BER test sequence error

**Alarm Generation**

No link, Remote fault

**Service Disruption Measurement**

Service disruption measurement activated as part of BER test

- Max./Avg. service disruption time, Resolution: 0.1 µs
- Number of service disruptions
- Disruption Type: Packet, LOS

**RFC 2544 Test**

Switch/Router test and Single ended network test modes:

- Throughput, Frame loss, Latency or Packet jitter, Back-to-back frames (burstability)
- End-to-end network test mode (two MT 1100A units in Local remote setup)
- Throughput, Frame loss, Back-to-back frames (burstability)
- Router latency test mode
- IP ping based latency test or packet jitter

**Service Activation Test (Y.1564)**

ITU-T Y.1564 Service Activation Test

- Up to 8 services per port
- Color-aware and non-color-aware in combinations (IP DSCP or VLAN PCP)
- Test modes: One-way (uni- or bi-directional, symmetrical or asymmetrical), Round-trip
- Verification against service acceptance criteria: Information rate, Frame transfer delay, Frame delay variation, Frame loss rate, Availability

Optional GPS timing synchronization

**Service Configuration Test**

Subtests for: Committed information rate, Excess information rate, Traffic policing, Committed burst size, Excess burst size

- Step duration: 1 s to 60 s (user programmable)
- Number of steps: 1 to 10 (user programmable)
- Slope: Rising/Falling
- Results: Pass/Fail indication, JR (Max./Min./Avg.), FL (Count/FLR), FTD, FDV (Max./Min./Avg./Current (during measurement))
### Service Performance Test
- All services tested simultaneously at CIR
- Duration: 15 min, 2 h, 24 h, user programmable
- Results: Pass/Fail indication, IR (Max./Min./Avg.), FL (Count/FLR), FTD, FDV (Max./Min./Avg./Current (during measurement)), AVAIL (%), Unavail (s)

### RFC 6349 TCP Throughput Test
**TCP Throughput Test**
TCP Throughput Test According to RFC 6349
- Supports connecting to iPerf server
- Test Direction Setup
  - Local → Remote
  - Remote → Local
  - Simultaneous in both directions
- For RFC 6349 test sequence, user can choose to measure for:
  - Path MTU
  - Baseline RTT
  - Window Scan and Throughput
  - Multi-Service
- Multi-Service: DSCP or TOS can be set to each TCP connection

#### Measurement results include:
- Auto-calculation of Bandwidth Delay Product (BDP)
- Transmitted and Retransmitted Bytes
- TCP Transfer Time Ratio
- TCP Efficiency
- Retransmitted Percentage
- Buffer Delay Percentage

### Cable Test
- Identifies cable faults like short circuits, or breaks in wire pair, and displays distance from instrument to fault

### Ping Test and Traceroute
**Ping Test**
For Connectivity and Configuration check
- Round Trip Time (RTT)
- Supports IPv4 and IPv6 addressing
- Answer incoming ping requests (On/Off)

**Traceroute**
Trace IP route over IP network
- User-defined Max. number of hops (1 to 255)
- Information per hop: Ping time (Max./Min./Avg.), Number of ping timeouts

### IP Channel Statistics
**Supported Bit Rate**
10 Mb/s, 100 Mb/s, 1 Gbps, 10 Gbps

**Statistics**
Statistics for up to 230 channels, identified by user-defined combinations of:
- IPv4, IPv6 or MAC address
- VLAN ID or MPLS label
- Protocol information
- IP next header (protocol)
- TCP/UDP ports

**Traffic capacity:**
- 10 Mb/s, 100 Mb/s, 1 Gbps, 10 Gbps, line speeds: 100% line load

**Available Information per channel:**
- Frame count/rate, Throughput, Byte count, MPLS frames, IP frame/packet size distribution, IP header bytes, IP fragments, TTL threshold violations, IP packet count/rate, IP bytes, IP throughput, IP header errors, TCP/UDP bytes, TCP/UDP packet count/rate, Throughput, TCP/UDP errored packets, Undersize frames, Oversize frames

### MPLS/MPLS-TP
**Number of MPLS Header**
Up to 8 MPLS headers set by user

**Parameters per MPLS Header**
- User-defined label, Exp and TTL fields in each MPLS header
- Address increment, Decrement and Random generation
- An EoMPLS (Ethernet over MPLS) or PWE3 (Pseudo-wire emulation edge-to-edge) label (RFC 4448 control word) can be added. MPLS can only transport VLAN if EoMPLS activated.

**Statistics**
- Number of labels (Max./Min.)
- Number of MPLS-TP frames
- Last received MPLS-TP label/priority/TTL

**OAM (MPLS-TP)**
ITU-T G.8113.1 comply
- Supported OAM messages
  - ITU-T Y.1731: CCM, LBM, LBR, LTM, LTR, AIS, LCK, TST, MCC, LMM, LMR, 1DM, DMM, DMR, EXM, EXR, VSM, VSR, SLM, SLR
  - IEEE 802.1ag: CCM, LBM, LBR, LTM, LTR

### PBB (Mac-in-Mac MiM)
**Programmable Field**
B-tag, I-tag, MAC destination and source addresses

**Result**
- Number of PBB frames, Last received B-tag VLAN ID, Last received B-tag priority, Last received I-tag priority, Last received I-tag service ID

**OAM**
- Supported OAM messages
  - ITU-T Y.1731: CCM, LBM, LBR, LTM, LTR, AIS, LCK, TST, MCC, LMM, LMR, 1DM, DMM, DMR, EXM, EXR, VSM, VSR, SLM, SLR
  - IEEE 802.1ag: CCM, LBM, LBR, LTM, LTR
## Ethernet Testing Specifications

### Ethernet OAM

<table>
<thead>
<tr>
<th>OAM Standards Supported</th>
<th>Messages Supported</th>
<th>IEEE 802.3ah Function</th>
<th>Statistics</th>
</tr>
</thead>
</table>
| • ITU-T Y.1731 (Service layer OAM)  
• IEEE 802.1ag (Connectivity layer OAM)  
• IEEE 802.3 (formerly IEEE 802.3ah) (Access link OAM) | Generates and receives following OAM messages.  
Supported OAM messages:  
• ITU-T Y.1731: CCM, LBM, LBR, LTM, LTR, AIS, LCK, TST, MCC, LMM, LMR, 1DM, DMM, DMR, EXM, EXR, VSM, VSR, SLM, SLR  
• IEEE 802.1ag: CCM, LBM, LBR, LTM, LTR  
• IEEE 802.3ah: Information, Variable request, Variable response, Loopback control | • Discovery  
• Loopback activate | • Number of each message generated/received |

### Synchronous Ethernet Test

<table>
<thead>
<tr>
<th>Supported Bit Rate</th>
<th>SyncE (ITU-T G.826x) Functionality</th>
<th>IEEE 1588 v2 Functionality</th>
</tr>
</thead>
</table>
| 10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps | Specify Quality Level (QL) of transmitted Ethernet signal. Analysis of QL indicated in received Ethernet signal with alarm at missing QL indications. SyncE results: SSM Rx count and rate, SSM Tx count, Indicated QL statistics, SSF seconds ESMC messages captured and exported in Wireshark format. | Each port of the Ethernet interface can act as a timing master or a timing slave independently.  
• Supported modes: Multicast (native PTP), Unicast (G.8265.1)  
• When acting as master in Unicast (G.8265.1) mode, one slave is accepted at a time. If the slave requires 32, 64, or 128 Sync messages per second, IEEE 1588-2008 paragraph 7.7.2.1 specifying 90% confidence interval is not followed.  
• Configurable parameters (per port): Clock identity, Port number, Priority 1, Priority 2, Domain number, Clock class, Slave only mode, Time source, Encapsulation, Announce receipt timeout, Clock accuracy, Clock step mode, Announce interval, Sync interval, Minimum delay request interval and Unicast duration. The UTC offset used when acting as clock master can be specified. |

### Ethernet Frame Capture

<table>
<thead>
<tr>
<th>Capture Buffer Size</th>
<th>Capture Frame Slicing</th>
<th>Include Tx Frame</th>
<th>Capture Trigger</th>
<th>Trigger Error</th>
<th>Trigger Condition Field</th>
<th>Capture Data</th>
</tr>
</thead>
</table>
| 1 Mbytes to 128 Mbytes (10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps)  
512 kbytes (40 Gbps, 100 Gbps) | If activated capture frame is first 64 bytes or 128 bytes of each frame (ignores rest of frame) | On/Off | Manual, On error, Field match  
Trigger position: Top, Middle | Fragmented frames  
Oversize frames  
Undersized frames  
Undersized and oversized frames  
FCS errored frames  
Any type | Enabled when capture trigger setting is field match  
• Offset: 0 to 15999 bytes  
• Length: 1 bytes to 16 bytes  
• Value: 16-byte data (max.) | Pcap format for display in Wireshark |

### 10G WAN-PHY

<table>
<thead>
<tr>
<th>WAN Mode</th>
<th>Terminology</th>
<th>Error Generation</th>
<th>Alarm Generation</th>
<th>Error Measurement</th>
<th>Alarm Detection</th>
<th>Overhead Byte Functionality</th>
</tr>
</thead>
</table>
| 10 Gbps Ethernet | SDH or SONET | SDH: A1A2, B1, B2, MS-REI, B3, HP-REI | SDH: LOF, OOF, MS-AIS, MS-RDI, MS-TIM, AU-AIS, AU-LOP, HP-PLM, HP-UNEQ, HP-TIM, HP-RDI, LCD | SDH: A1A2, B1, B2, MS-REI, B3, HP-REI | SDH: LOS, LOF, OOF, MS-AIS, MS-RDI, MS-TIM, AU-AIS, AU-LOP, HP-PLM, HP-UNEQ, HP-TIM, HP-RDI, LCD, LSS | Generation of user-defined overhead bytes  
Capture and display of current overhead bytes |

### Reflector

| Reflector Mode | The following parameters are user selectable:  
• Reflector MAC/IP address  
• Swap all MAC addresses or one specific MAC address  
• Swap IP addresses  
• Swap port numbers on UDP/TCP frames  
• Force ACK on TCP frames  
• Answer incoming ARP, Ping requests |

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CPRI/OBSAI Testing (Options MU100010A-071, MU100010A-072, MU100010A-073)

<table>
<thead>
<tr>
<th>CPRI/OBSAI Testing</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Port Mode</td>
<td>Off, Normal, Through</td>
</tr>
</tbody>
</table>
| Line Rate          | CPRI: 614.4, 1228.8, 2457.6, 3072.0, 4915.2, 6144.0, 9830.4, 10137.6 Mbps  
                     OBSAI: 768, 1536, 3072.0, 6144.0 Mbps |
| Transmitter Clock  | Reference Clock  
                     • Internal clock  
                     • External clock  
                     • BITS  
                     • SETS  
                     • 2 MHz  
                     • 10 MHz  
                     • GPS  
                     • Received clock |
| Content            | Unframed, CPRI Link |
| Pattern            | PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, User 32 bits, Off |
| CPRI Link          | Start up: Enabled, Disabled  
                     Role: Master, Slave  
                     Protocol version: 1, 2  
                     HDLC rate: no HDLC, 240, 480, 960, 1920, 2400 kbit/s, Highest possible  
                     Ethernet: On, Off; Pointer: 20 to 63 |
| Alarm Insertion    | Signal Loss, LOS, LOF, PSL, Remote-LOS, Remote-LOF, RAI, SDI, Reset |
| Error Insertion    | Item: LCV, SHV, K30.7, Pattern error, Insertion timing: Manual, Rate |
| Frequency Offset   | ±100 ppm (1 ppm steps) |
| Alarm Detection    | Signal loss, LOS, LOF, PSL |
| Error Detection    | LCV, SHV, K30.7, Pattern error |
| Remote Status      | Remote LOS, Remote LOF, RAI, SDI, Reset |
| Link               | Rx: Protocol version, HDLC rate, Pointer P  
                     Tx: Protocol version, HDLC rate, Pointer P |
| BER Test           | Alarms: Signal loss, LOS, LOF, PSL, Remote LOS, Remote LOF, RAI, SDI, Reset  
                     Errors: LCV, SHV, K30.7, Pattern error  
                     Frames count: Rx hyper frame, Rx code words, Tx hyper frame, Tx code words  
                     Delay: Delay, Average Delay, Min. Delay, Max. Delay  
                     Measurement count |
| APS                | APS (Automatic Protection Switching) test and analysis  
                     • APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted.  
                     • Trigger events (user selectable)  
                     • Alarm: Signal Loss, LOS, LOF  
                     • Error: LCV, SHV, Pattern error  
                     • Remote Alarm: Remote LOS, Remote LOF, RAI, SDI, Reset  
                     • Switching time: Switching count, Pass/Fail, Minimum, Maximum and Average can be displayed.  
                     • APS switching time measurement resolution: 1 μs |
| Pass Through       | Alarms: Signal loss, LOS, LOF, PSL, Remote LOS, Remote LOF, RAI, SDI, Reset  
                     Errors: LCV, SHV, K30.7, Pattern error |

Fibre Channel Testing

<table>
<thead>
<tr>
<th>Fibre Channel Test</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Supported FC Bit Rate</td>
<td>1.0625 Gbps (FC-100/1GFC), 2.125 Gbps (FC-200/2GFC), 4.25 Gbps (FC-400/4GFC), 8.5 Gbps (FC-800/8GFC), 10.52 Gbps (FC-1200/10GFC)</td>
</tr>
<tr>
<td>Mode</td>
<td>Terminate, Monitor</td>
</tr>
<tr>
<td>Topology</td>
<td>Point-to-point, Fabric</td>
</tr>
<tr>
<td>Primitive Sequence Protocol</td>
<td>Count and transmit primitive sequence: LR, LRR, NOS, OLS</td>
</tr>
</tbody>
</table>
| Flow Control       | Credit based transmitter: On/Off  
                     Buffer-to-buffer credit configuration: 1 to 65535, Buffer-to-buffer credit and R_RDY counters, R_RDY injection |
| Traffic Generation | • 1GFC (with SOF and EOF frame delimiters and 2GFC frames), Class-3 service frames  
                     • Traffic shaping: Constant, Ramp, Burst, 2GFC frame header configuration  
                     • Frame length configuration: 3240 bytes (max.) |
| BER Test           | Test modes: Unframed BER test, Layer 1 BER test, Layer 2 BER test  
                     Test patterns: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, HF test pattern, CSPAT, CJPAT, CRPAT, JTPAT, SPAT, 55 Hex, Fox, 32-bit user programmable  
                     Error injection: Bit, CRC, Symbol  
                     Results: Pattern loss seconds, Traffic loss seconds, Bit error count, BER |
| Measurement        | Alarm detection: LOS, Link down, Pattern loss  
                     • Service disruption measurement: Average/Max service disruption, Number of service disruptions  
                     • Traffic statistics: Bandwidth utilization, Data rate, Frame count, Byte count, Frame size distribution, Buffer-to-buffer credit count, R_RDY count, Frame loss count, Round trip delay, Packet jitter, Bit errors, CRC errors, Symbol errors, LR, LRR, NOS, OLS |

Device Testing

<table>
<thead>
<tr>
<th>Device Test</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface Type</td>
<td>Off, CFP, CFP2, CXP, QSFP+, CFP4</td>
</tr>
<tr>
<td>Supported Bit Rate</td>
<td>Off, 40 Gbps Ethernet, 100 Gbps Ethernet, STM-256/OC-78, OTU4, OTU3, OTU3e1, OTU3e2</td>
</tr>
<tr>
<td>Timing Source</td>
<td>Internal clock, External clock</td>
</tr>
<tr>
<td>Test Pattern</td>
<td>PRBS 7, PRBS 9, PRBS 15, PRBS 23, PRBS 31, Square wave</td>
</tr>
<tr>
<td>Frequency Offset</td>
<td>±200 ppm (0.1 ppm step)</td>
</tr>
</tbody>
</table>
SDH/SONET and PDH/DSn Testing

SDH and SONET Test

Frame

SDH: Complies with ITU-T G.707, SONET: Complies with Telcordia GR-253

Transmitter Clock

- Internal clock accuracy: ±4.6 ppm, Clock offset: ±200 ppm (0.1 ppm steps)
- Recovered clock
- TTL level external 2 MHz clock
- SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)

Receive Signal Rate

±200 ppm

Frequency deviation indication resolution: ±0.1 ppm

STM-1e Electrical Attenuation and Impedance Mode

TERMINATE: Up to 12 dB cable attenuation, Nominal impedance

MONITOR: 20 dB linear attenuation and up to 12 dB cable attenuation, Nominal impedance

TCM Frame Format

ITU-T G.783, G.707 Annex D (TCM option 2) and Annex E, POH bytes:
- N1 (VC-4, VC-3), Z5 (STS-3c, STS-1), N2 (VC-12, VC-11), Z6 (VT-2, VT-1.5)

TCM Access Point Identifier (Apid): 15 bytes ASCII sequence, CRC-7

Scrambling

SDH: Complies with ITU-T G.707, SONET: Complies with Telcordia GR-253

SDH Mapping

See page 7

Alarms

Detected and generated alarms
- SDH: LOS, LOF, OOF, MS-AIS, MS-RDI, AU-AIS, AU-LOP, HP-PLM, HP-UNEQ, HP-TIM, HP-RDI, TU-LOM, TU-AIS, TU-LOP, LP-PLM, LP-UNEQ, LP-TIM, LP-RDI, LSS
- STM-256: LOF-STL, OOF-STL, LOR-STL, OOR-STL
- OC-768: LOF-STL, OOF-STL, LOR-STL, OOR-STL
- TCM: TC-LTC, TC-TIM, TC-UNEQ, TC-AIS, TC-RDI, TC-ODI, STL (STM-256)

Inserted alarms
- Permanent
- Alternate: 1 to 8000 consecutive alarm frames, 1 to 8000 consecutive normal frames

Errors

Detected and generated errors
- SDH: A1/A2, B1, B2, MS-REI, B3, HP-REI, VS/B3, LP-REI, Pattern error, ERR trans
- STM-256: A1A2-STL
- OC-768: A1A2-STL
- TCM: TC-IEC, TC-BIP2, TC-REI, TC-OEI

Error insertion
- Manual: 1 to 8000 consecutive errors (excluding Pattern error)
- 1 to 4000 consecutive errors (for Pattern error)
- Continuous: 10^-1, 10^-2, 10^-3, 10^-4, 10^-5, 10^-6, 10^-7, 10^-8, 10^-9 (The available highest rate varies depending on the error item.)
- Alternate: 1 to 8000 consecutive error frames, 1 to 8000 consecutive normal frames (excluding Pattern error and ERR trans)
- 1 to 4000 consecutive error bits, 100 to 4000 consecutive normal bits (for Pattern error)

BER Test Pattern

Pattern generation and detection for O.181 bulk test pattern
- Test patterns supported: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, PRBS patterns can be inverted.
- All 0 s, All 1 s, Alternating 1:1, Alternating 1:3, Alternating 1:7, 2 in 8
- User-defined patterns (Pattern length: up to 2048, Length step: 8-bit)

Pointer

Support pointer events monitoring and generation
- Pointer test sequences: None, Single alternating, Regular + Double, Regular + Missing, Double alternating
- Display pointer value of receiver side
- Graphical display of pointer movements

Overhead

- Generation of section/transport and path overhead bytes
- Display of current section/transport and path overhead bytes

All overhead can be decoded, including decoded J0, J1, J2 byte.

STL Skew

STM-256, OC-768
- Insertion
- Bits: 0 to 138240
- Detection
- Relative skew, Marker map

Through Mode

- Transparent mode
- OH overwrite mode: Can be changed SOH (SDH), TOH (SONET)
SDH and SONET Results

| Status | Current information on:  
| - Alarms and errors on monitored line  
| - Input level indication for optical signals  
| - Input level indication for electrical signals  
| - Actual bit rate  
| - Frequency deviation |

| Statistics | User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h  
| Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Pointer operations  
| Event log: Major measurement events incl. errors, alarms and pointer operations are logged with 1-second resolution. |

| Error Performance | G.826/G.828/G.829/M.2100 analysis of received signal based on detected errors and alarms:  
| ES, SES, BBE (not M.2100), UNAV |

| APS | APS (Automatic Protection Switching) test and analysis  
| • Trigger events (user selectable):  
| - SDH: SDH alarms and errors, pattern bit error, APS switchover  
| - SONET: SONET alarms and errors, pattern bit error, APS switchover  
| - Number of switchovers indicated by APS protocol  
| - K1/K2 bytes set and displayed  
| • Resolution of APS switching time measurement, SDH  
| - SDH events excluding VC-12 and VC-11 events, LOS (Loss of Signal): 1 µs  
| - VC-12 and VC-11 events: 0.5 ms  
| - Resolution of APS switching time measurement, SONET  
| - SONET events excluding VT-1.5 and VT-2 events, LOS (Loss of Signal): 1 µs  
| - VT-1.5 and VT-2 events: 0.5 ms |

| Round Trip Delay (Propagation Time) Measurement | Resolution: 0.1 µs  
| Measured Max. time: 10.0 s  
| Interval: 0.5, 1, 2, 5, 10 s |

| Tributary Scan | Displays the alarm status of all channels in a specified layer except STM-256/OC-768  
| Green: No alarm  
| Red: Alarm occurring  
| Gray: Not applied |

| E1 Test | Electrical line interfaces: 2 ports (MU110010A-001)  
| Connector: BNC or RJ48 (selectable)  
| Complies with ITU-T G.703 for 2048 kbps  
| Supported input impedances:  
| - 75Ω (unbalanced), 120Ω (balanced), High (>10 × nominal)  
| Line Code | HDB3 or AMI  
| Framing | Unframed or Framed: FAS/nFAS, Transmitter: Sa-bits (non-FAS), User-programmable  
| Transmitter Clock | Internal 2.048 Mbps clock accuracy: ±4.6 ppm, Clock offset: ±125 ppm (1 ppm steps)  
| • Recovered from receiver  
| • TTL level external 2.048 MHz clock  
| • SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)  
| Receive Signal Rate | 2048 kbps ±150 ppm  
| Frequency deviation indication accuracy: ±1 ppm  
| Receiver Attenuation and Impedance Mode | TERMINATE  
| • Up to 40 dB cable attenuation, Nominal impedance  
| MONITOR  
| • 20 to 26 dB linear attenuation and up to 6 dB cable attenuation, Nominal impedance  
| • 20 to 30 dB linear attenuation, 0 dB cable attenuation, Nominal impedance  
| BRIDGED  
| • Up to 40 dB cable attenuation, High impedance |

| Drop and Insert | Supports drop & insert of one or multiple 64 kbps timeslots (TS) within E1  
| Alarms | Detected and generated alarms:  
| - No signal, AIS, No frame, Distant (RDI) alarm, Pattern sync. loss, No CAS, MFAS, Distant (RDI) MF alarms  
| Errors | Detected: FAS/nFAS, CRC4, E-bit, Code, Pattern, Pattern slips, Frame slips  
| Generated: FAS bit, FAS word, CRC-4, E-bit, Code, Pattern, Transparent  
| Error insertion |  
| - Manual: 1 to 255 consecutive errors (1 to 16 consecutive FAS word errors)  
| - Continuous: 10⁻², 10⁻⁴, 10⁻⁶, 10⁻⁸, 10⁻¹⁰  
| • Provoking of G.821, G.826 or M.2100 events (ES, SES etc.) (FAS, Pattern, CRC-4, E-bit)  
| Manual slip insertion: Frame slips, Pattern slips |

| BER Test Pattern | Pattern generation:  
| • Unframed or Framed: n × 64 kbps in contiguous or non-contiguous channel access  
| Supported test patterns:  
| • PRBS 6, PRBS 7, PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, QRSS 11, QRSS 20  
| • Fox pattern, Fox (CMA 3000), All 0, All 1, Alternating (1:1), (1:3), (1:7), (3:24)  
| • User-defined up to 32 bits (Length: 1-bit steps)  
| • User-defined up to 2048 bits (Length: 8-bit steps)  
| • All patterns can be inverted, except user-defined  
| CAS | CAS signaling bits can be set.  
| Tone and Speech Signal Insertion | Tone in one speech channel on one transmitter  
| • Frequency: 1 Hz to 4 kHz (1-Hz steps)  
| • Level: –70 to +3 dBm (1-dBm steps)  
| • Artificial speech signal  
| Speech Decode | 64 kbps (ITU-T G.703): A-law according to ITU-T G.711 |
### E1 Results

**Status**
- Current Information on
  - Alarms and errors on monitored line
  - Input level indication
  - Actual bit rate
  - Frequency deviation
  - FAS/non-FAS and CAS bits
  - Traffic overview: Busy/Idle indication from all 31 channels

**Time Slot Monitoring**
- Contents of single time slot including positive/negative peak values.
  - Frequency for encoded tone: 1 Hz to 4 kHz (1-Hz steps)
  - Level for encoded tone: –66 to +3 dBm (1-dBm steps)

**Statistics**
- User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h
  - Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation information
  - Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.

**Error Performance**
- G.821, G.826 or M.2100 analysis of PRBS in received signal, or based on CRC-4, E-bit or FAS: ES, SES, BBE (G.826), UAT, EPS, AT % or count.
  - Error performance evaluation for total measurement:
    - HR% for user-defined error performance parameter or programmable OK and not-OK limits for FAS, Pattern, CRC-4 or E-bit count or ratio

**APS**
- APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted.
  - Number of switchovers.
  - Trigger events (User selectable): 2 Mbps alarms (LOF or AIS; pattern bit error)
  - Resolution of APS switching time measurement: LOF and AIS: 0.25 ms

**Round Trip Delay (Propagation Time) Measurement**
- Resolution: 1 µs
  - Measured Max. time: 10.0 s
  - Interval: 0.5, 1, 2, 5, 10 s

### DS1 Test

**Test Port**
- Electrical line interfaces: 2 ports (MU110010A-001)
  - Connector: Bantam

**General**
- Complied with ANSI T1.102 for 1544 kbps.

**Impedance**
- 100Ω or High (10 x nominal; Receiver only) and DSX MON 100Ω ±1%

**Line Code**
- B8ZS, AMI

**Framing**
- Unframed or Framed, Framed: SF, ESF, J-ESF (J1)

**Transmitter Clock**
- Internal 1.544 Mbps clock accuracy: ±4.6 ppm, Clock offset: ±125 ppm (1 ppm steps)
  - Recovered from receiver
  - TTL level external 2.048 MHz clock
  - SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)

**Line Build Out**
- 0, –7.5, –15, –22.5 dB
  - 0 to 133 ft, 133 to 266 ft, 266 to 399 ft, 399 to 533 ft, 533 to 655 ft

**Receive Signal Rate**
- 1544 kbps ±150 ppm
- Frequency deviation indication resolution: ±1 ppm

**Receiver Sensitivity**
- DS1 Short Haul
  - +15 dB linear attenuation, 0 dB cable attenuation, Nominal impedance
  - TERMINATE
  - Up to 36 dB cable attenuation, Nominal impedance
  - DSX MONITOR
  - 15 to 25 dB linear attenuation, Nominal impedance
  - BRIDGE
  - Up to 36 dB cable attenuation, High impedance

**Drop and Insert**
- Drop & Insert of one or multiple 56 kbps or 64 kbps timeslots (TS) within DS1

**Alarms**
- Generated and detected: LOS, OOF, AIS (Blue), RAI (Yellow), LSS

**Errors**
- Generated or detected: Pattern, F-bit, S-bit, Pattern slips, BPV (Code), CRC-6, EXZ
  - Error insertion
  - Manual: 1 to 255 consecutive errors
  - Continuous: 10–2, 10–3, 10–4, 10–5, 10–6, 10–7
  - For performance: ES, SES

**BER Test Pattern**
- Supported test patterns
  - PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, QRSS 20
  - All 0, All 1, Alternating (1:1), (1:3), (1:7), (3:24), Fox pattern, Fox (CMA 3000)
  - User-defined up to 32 bits (Length: 1-bit steps)
  - User-defined up to 2048 bits (Length: 8-bit steps)
  - All patterns can be inverted, except User-defined

**Loopback Code**
- Supported loopback codes: LLA, LLD, PLA, PLD, ULB, NLA, USR, ACS, DCS, AN1, DN1, AN2, DN2, 100K
- USER_INBAND (User-defined FDL/in-band code)
  - Insertion: On/Off

**CAS**
- CAS signaling bits can be set.

**Tone and Speech Signal Insertion**
- Tone in one speech channel on one transmitter
  - Frequency: 1 Hz to 4 kHz (1-Hz steps)
  - Level: –70 to +3 dBm (1-dBm steps)
  - Artificial speech signal

**Speech Decode**
- 64 kbps or 56 kbps: μ-law
## DS1 Results

### Status
- Current Information on
  - Alarms and errors on monitored line
  - Input level indication
  - Actual bit rate
  - Frequency deviation
  - Contents of one time slot
  - Framing and CAS bits
  - Traffic overview: Busy/Idle indication from all 24 channels

### Time Slot Monitoring
- Contents of single time slot including positive/negative peak values.
  - Frequency for encoded tone: 1 Hz to 4 kHz (1-Hz steps)
  - Level for encoded tone: –66 to +3 dBm (1-dBm steps)

### Statistics
- User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h
- Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation information
- Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.

### Error Performance
- G.821, G826, or M.2100 analysis of PRBS in received signal, or based on detected errors: ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)

### APS
- APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted.
- Number of switchovers.
- Trigger events (User selectable): 1.5 Mbps alarms (OOF, AIS; pattern bit error)
- APS switching time measurement resolution: No frame, AIS: 0.25 ms

### Round Trip Delay (Propagation Time) Measurement
- Resolution: 1 µs
- Measured Max. time: 10.0 s
- Interval: 0.5, 1, 2, 5, 10 s

## E3 Test

### Test Port
- Electrical line interfaces: 2 ports (MU110010A-001)
- Connector: BNC

### General
- Complies with ITU-T G.703 for 34368 kbps

### Impedance
- 75Ω

### Line Code
- HDB3

### Framing
- Unframed or Framed: Complies with ITU-T G.751 for E3 signals

### Transmitter Clock
- Internal clock accuracy: ±4.6 ppm, Clock offset: ±125 ppm (1 ppm steps)
- Recovered from receiver
- TTL level external 2.048 MHz clock
- SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)

### Receive Signal Rate
- 34368 kbps ±150 ppm
- Frequency deviation indication resolution: ±1 ppm

### Attenuation and Impedance Mode
- TERMINATE
  - Up to 12 dB cable attenuation, Nominal impedance
- MONITOR
  - 20 dB linear attenuation and up to 12 dB cable attenuation, Nominal impedance
  - 20 to 30 dB linear attenuation, 0 dB cable attenuation, Nominal impedance

### Alarms
- Detected and generated alarms: No signal, AIS, No frame, RDI, Pattern sync. loss

### Errors
- Detected and generated errors: Frame, Code, Pattern, Pattern slip
- Error insertion
  - Manual: 1 to 255 consecutive errors
  - Continuous: 10⁻³, 10⁻⁴, 10⁻⁵, 10⁻⁶, 10⁻⁷
- For performance: ES, SES

### BER Test Pattern
- Pattern Generation and Detection, Supported test patterns
  - PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23
  - Fox pattern, Fox (CMA 3000), All 0, All 1, Alternating 1:1, Alternating 1:3, Alternating 1:7, Alternating 3:24
  - User-defined up to 32 bits (Length: 1-bit steps)
  - User-defined up to 2048 bits (Length: 8-bit steps)
- All patterns can be inverted, except user-defined

## E3 Results

### Status
- Current Information on
  - Alarms and errors on monitored line
  - Input level indication
  - Actual bit rate
  - Frequency deviation

### Statistics
- User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h
- Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation information
- Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.

### Error Performance
- G.826/M.2100 analysis of received signal, or based on detected errors ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)

### Round Trip Delay (Propagation Time) Measurement
- Resolution: 1 µs
- Measured Max. time: 10.0 s
- Interval: 0.5, 1, 2, 5, 10 s
## DS3 Test

<table>
<thead>
<tr>
<th>Test Port</th>
<th>Electrical line interfaces: 2 ports (MU110010A-001)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Connector</td>
<td>BNC</td>
</tr>
<tr>
<td>General</td>
<td>Complies with ANSI for 44736 kbps</td>
</tr>
<tr>
<td>Impedance</td>
<td>75Ω</td>
</tr>
<tr>
<td>Line Code</td>
<td>B325</td>
</tr>
<tr>
<td>Framing</td>
<td>Unframed or Framed: C-bit parity, M13 in accordance with ANSI T1.107</td>
</tr>
</tbody>
</table>
| Transmitter Clock | • Internal clock accuracy: ±4.6 ppm, Clock offset: ±125 ppm (1 ppm steps)  
  • Recovered from receiver  
  • TTL level external 2.048 MHz clock  
  • SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps) |
| Line Build Out | 0 ft, 225 ft                                      |
| Receive Signal Rate | 44736 kbps ±150 ppm                              |
| Attenuation and Impedance Mode | TERMINATE                                      |
| Alarms | Detected and generated alarms: LOS, LOF, AIS (Blue), RAI (Yellow), DS3 idle, LSS |
| Errors | Detected and generated errors: Pattern, C-bit, F-bit, P-bit, Code (BPV), FEBE (detect only), EXZ (detect only)  
  Error insertion: Manual: 1 to 255 consecutive errors  
  Continuous: 10^-2, 10^-3, 10^-4, 10^-5, 10^-6, 10^-7 |
| BER Test Pattern | Pattern generation and detection, Supported test patterns  
  • PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, QRSS 20  
  • Fox pattern, Fox (ICMA 3000), All 0, All 1, Alternating 1:1, Alternating 1:3, Alternating 1:7, Alternating 3:24  
  • User-defined up to 32 bits (Length: 1-bit steps)  
  • User-defined up to 2048 bits (Length: 8-bit steps)  
  All patterns can be inverted, except user-defined |

## E4 Test

<table>
<thead>
<tr>
<th>Test Port</th>
<th>Electrical line interfaces: 2 ports (MU110010A-001)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Connector</td>
<td>BNC</td>
</tr>
<tr>
<td>General</td>
<td>Complies with ITU-T G.703 for 139264 kbps interfaces</td>
</tr>
<tr>
<td>Impedance</td>
<td>75Ω</td>
</tr>
<tr>
<td>Line Code</td>
<td>CMI</td>
</tr>
<tr>
<td>Framing</td>
<td>Unframed or Framed: Complies with ITU-T G.751 for E4 signals</td>
</tr>
</tbody>
</table>
| Transmitter Clock | • Internal clock accuracy: ±4.6 ppm, Clock offset: ±125 ppm (1 ppm steps)  
  • Recovered from receiver  
  • TTL level external 2.048 MHz clock  
  • SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps) |
| Receive Signal Rate | 139264 kbps ±150 ppm                              |
| Attenuation and Impedance Mode | TERMINATE                                      |
| Alarms | Detected and generated alarms: No signal, AIS, No frame, RDI, Pattern sync. loss |
| Errors | Detected and generated errors: Frame, Pattern error, Pattern slips  
  Error insertion: Manual: 1 to 255 consecutive errors  
  Continuous: 10^-2, 10^-3, 10^-4, 10^-5, 10^-6, 10^-7 |
| BER Test Pattern | Pattern generation and detection, Supported test patterns  
  • PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, QRSS 20  
  • All 0, All 1, Alternating 1:1, Alternating 1:3, Alternating 1:7, Alternating 3:24  
  • User-defined up to 32 bits (Length: 1-bit steps)  
  • User-defined up to 2048 bits (Length: 8-bit steps)  
  All patterns can be inverted, except user-defined |
Optical Module Adaptors Specifications

**E4 Results**

<table>
<thead>
<tr>
<th>Status</th>
<th>Current information on</th>
</tr>
</thead>
<tbody>
<tr>
<td>• Alarms and errors on monitored line</td>
<td></td>
</tr>
<tr>
<td>• Input level indication</td>
<td></td>
</tr>
<tr>
<td>• Actual bit rate</td>
<td></td>
</tr>
<tr>
<td>• Frequency deviation</td>
<td></td>
</tr>
</tbody>
</table>

| Statistics | User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h |
| Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation |
| Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution. |

**Error Performance**

G.826/M.2100 analysis of received signal, or based on detected errors ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)

**Round Trip Delay (Propagation Time) Measurement**

Resolution: 1 µs
Measured Max. time: 10.0 s
Interval: 0.5, 1, 2, 5, 10 s

---

### 10 Lane Extender MZ1223C

<table>
<thead>
<tr>
<th>Interface/Connector</th>
<th>Host side: CFP MSA Draft 1.4 Compatible Interface</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Network side: SMP (plug) × 46</td>
</tr>
<tr>
<td>Insertion/Removal Cycles (max.)</td>
<td>Host side: 180 (CFP connector)</td>
</tr>
<tr>
<td></td>
<td>Network side: 480 (SMP connector)</td>
</tr>
<tr>
<td>Insertion Loss</td>
<td>≤3.5 dB @ 5.59050 GHz (1/2 × 11.1809793568 Gbps) Including connector</td>
</tr>
<tr>
<td>Telecomcommunications Quality</td>
<td>Bit Error Rate: 1.0E-13 or less</td>
</tr>
<tr>
<td></td>
<td>for Evaluation: Installed in MT1100A, Loopback via 30 cm Semi-rigid cable</td>
</tr>
<tr>
<td>Bit rate: 11.1809793568 Gbps × 10 lanes</td>
<td></td>
</tr>
<tr>
<td>Pattern: PRBS31</td>
<td></td>
</tr>
<tr>
<td>Dimensions and Mass</td>
<td>160 (W) × 59.7 (H) × 218.4 (D) mm (excluding projections), ≤2 kg</td>
</tr>
<tr>
<td>Environmental</td>
<td>Temperature range</td>
</tr>
<tr>
<td>Operating: +10° to +30°C, Storage: -20° to +60°C</td>
<td></td>
</tr>
</tbody>
</table>

*: Each I/O of Tx10p, Tx10n, Rx10p, and Rx10n is not connected with MT1100A when MZ1223C is installed in MT1100A.

*: Tx/Rx indicates transmission signal/reception signal. p/n indicates Positive/Negative sides for a differential interface.

*: MZ1223C and MT1100A are DC Coupled, and the capacitor for the AC coupling is not arranged in MZ1223C and MT1100A.

---

### 4 Lane Extender for CFP2 J1666A

<table>
<thead>
<tr>
<th>Interface/Connector</th>
<th>Host side: CFP2 Plug connector</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Network side: SMPM (GPPO) × 8 (TX/RX differential)</td>
</tr>
<tr>
<td>Insertion/Removal Cycles</td>
<td>Host Side: 50 times (CFP2 Plug)</td>
</tr>
<tr>
<td></td>
<td>SMPM Side: 100 times (SMPM connector)</td>
</tr>
<tr>
<td>Insertion Loss (S21)</td>
<td>≤-5.5 dB @ 14 GHz*</td>
</tr>
<tr>
<td>Return Loss (S11) SMPM side</td>
<td>≤-10 dB @ 14 GHz*</td>
</tr>
<tr>
<td>Skew between Pair Connectors</td>
<td>≤3 ps</td>
</tr>
<tr>
<td>Dimensions and Mass</td>
<td>138 (W) × 80.6 (H) × 15.8 (D) mm (excluding projections), ≤115 g</td>
</tr>
<tr>
<td>Environmental</td>
<td>Temperature range</td>
</tr>
<tr>
<td>Operating: 0° to +40°C, Storage: -20° to +60°C</td>
<td></td>
</tr>
</tbody>
</table>

*: Defined as the total loss when connecting with HCB (Host Compliance Board) or MCB (Module Compliance Board) that conforms to CEI-28G-VSR.

---

### CFP2-CFP4 Adaptor J1665A

<table>
<thead>
<tr>
<th>Interface/Connector</th>
<th>Host Side: CFP2 Plug connector</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>CFP4 Side: CFP4 Host adaptor</td>
</tr>
<tr>
<td>Insertion/Removal Cycles</td>
<td>Host Side: 50 times (CFP2 connector)</td>
</tr>
<tr>
<td></td>
<td>CFP4 Side: 100 times (CFP4 connector)</td>
</tr>
<tr>
<td>Insertion Loss (S21)</td>
<td>≤-8.5 dB @ 14 GHz*</td>
</tr>
<tr>
<td>Return Loss (S11) CFP4 Side</td>
<td>≤-5 dB @ 14 GHz*</td>
</tr>
<tr>
<td>Skew between Pair Connectors</td>
<td>≤4 ps</td>
</tr>
<tr>
<td>Dimensions and Mass</td>
<td>106.5 (W) × 41.5 (H) × 14.8 (D) mm (excluding projections), ≤120 g</td>
</tr>
<tr>
<td>Environmental</td>
<td>Temperature range</td>
</tr>
<tr>
<td>Operating: 0° to +40°C, Storage: -20° to +60°C</td>
<td></td>
</tr>
</tbody>
</table>

*: Defined as the total loss when connecting with HCB (Host Compliance Board) or MCB (Module Compliance Board) that conforms to CEI-28G-VSR.

---

### CFP2-QSFP28 Adaptor J1686B

<table>
<thead>
<tr>
<th>Interface/Connector</th>
<th>Host Side: CFP2 Plug connector</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>QSFP28 Side: QSFP28 Host adaptor</td>
</tr>
<tr>
<td>Insertion/Removal Cycles</td>
<td>Host Side: 50 times (CFP2 connector)</td>
</tr>
<tr>
<td></td>
<td>QSFP28 Side: 100 times (QSFP28 connector)</td>
</tr>
<tr>
<td>Insertion Loss (S21)</td>
<td>≤-12 dB @ 14 GHz*</td>
</tr>
<tr>
<td>Return Loss (S11) QSFP28 Side</td>
<td>≤-4 dB @ 14 GHz*</td>
</tr>
<tr>
<td>Dimensions and Mass</td>
<td>106.5 (W) × 41.5 (H) × 14.8 (D) mm (excluding projections), ≤120 g</td>
</tr>
<tr>
<td>Environmental</td>
<td>Temperature range</td>
</tr>
<tr>
<td>Operating: 0° to +40°C, Storage: -20° to +60°C</td>
<td></td>
</tr>
</tbody>
</table>

*: Defined as the total loss when connecting with HCB (Host Compliance Board) or MCB (Module Compliance Board) that conforms to CEI-28G-VSR.
## Ordering Information

Please specify the model/order number, name and quantity when ordering.
The names listed in the chart below are Order Names. The actual name of the item may differ from the Order Name.

### 1. Mainframe

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MT1100A</td>
<td>Network Master Flex</td>
</tr>
</tbody>
</table>

**Standard accessories for MT1100A**

- Z1746A: Stylus
- Z1870A: Utilities ROM
- W3734AE: MT1100A Quick Reference Guide (English)
- Z1861A: Carry Case
- Z1862A: Module Combination Kit
- B0699A: Soft Case

**Option**

MT1100A-003*: Connectivity for WLAN/Bluetooth

*1: Please visit the Anritsu web site for updated information.

### 2. Power Supply Module

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MU110001A**</td>
<td>Battery and AC Power Supply Module</td>
</tr>
<tr>
<td>MU110002A**</td>
<td>AC only High Power Supply Module</td>
</tr>
</tbody>
</table>

**Standard accessories for MU110001A**

- G0327A*: Li-Ion Battery: 2 pcs

*2: Select MU110001A or MU110002A. When installing two test modules in an MT1100A mainframe, one module must be an MU110010A to select MU110001A, battery powered power module.

*3: MU110001A requires two G0327A.

### 3. Measurement Module**

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MU110010A</td>
<td>10G Multirate Module</td>
</tr>
<tr>
<td>MU110011A</td>
<td>100G Multirate Module</td>
</tr>
<tr>
<td>MU110013A</td>
<td>40/100G Advanced Module</td>
</tr>
</tbody>
</table>

**4: One or two modules of MU110010A/11A/13A can be installed in one mainframe.

### 4. Protocol Options*, **

#### MU110010A

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MU110010A-001</td>
<td>Up to 10G Single Channel</td>
</tr>
<tr>
<td>MU110010A-011</td>
<td>Ethernet 40G Single Channel</td>
</tr>
<tr>
<td>MU110010A-012</td>
<td>Ethernet 40G Dual Channel</td>
</tr>
<tr>
<td>MU110010A-020</td>
<td>TCP Throughput</td>
</tr>
</tbody>
</table>

**CPR/ OBSAI**

- MU110010A-071: CPR/ OBSAI Up to 5G Dual Channel
- MU110010A-072: CPR/ OBSAI 6G to 10G Single Channel
- MU110010A-073: CPR/ OBSAI 6G to 10G Dual Channel

**OTN**

- MU110010A-001: Up to 2.7G Dual Channel
- MU110010A-051: OTN 10G Single Channel
- MU110010A-052: OTN 10G Dual Channel
- MU110010A-061: ODU Multiplexing
- MU110010A-062: ODU Flex

**SDH/SONET**

- MU110010A-001: Up to 2.7G Dual Channel
- MU110010A-081: STM-64 OC-192 Single Channel
- MU110010A-082: STM-64 OC-192 Dual Channel

**Fibre Channel**

- MU110010A-002: FC 1G 2G 4G Dual Channel
- MU110010A-091: FC 8G 10G Single Channel
- MU110010A-092: FC 8G 10G Dual Channel

*5: "channel" means physical port or client signal test mapped in OTN. Refer to page 5 to 7 for OTN and client signals.

*6: These options can be retrofitted. The Model/Order No. of retrofit options is "-3**".

Example: MU110010A-001 Up to 2.7G Dual Channel becomes MU110010A-301 Up to 2.7G Dual Channel Retrofit. In addition, specify one of the following media along with the required option.

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z1849A</td>
<td>DVD-ROM for Retrofit Options</td>
</tr>
<tr>
<td>Z1850A</td>
<td>USB Stick for Retrofit Options</td>
</tr>
</tbody>
</table>

### MU110011A

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MU110011A-001</td>
<td>Up to 10G Single Channel</td>
</tr>
<tr>
<td>MU110011A-003</td>
<td>Ethernet 40G Single Channel</td>
</tr>
<tr>
<td>MU110011A-013</td>
<td>Ethernet 40G Dual Channel</td>
</tr>
<tr>
<td>MU110011A-014</td>
<td>Ethernet 40G Dual Channel</td>
</tr>
<tr>
<td>MU110011A-015</td>
<td>Ethernet 100G Single Channel</td>
</tr>
<tr>
<td>MU110011A-020</td>
<td>TCP Throughput</td>
</tr>
</tbody>
</table>

**CPR/ OBSAI**

- MU110011A-071: CPR/ OBSAI Up to 10G Single Channel
- MU110011A-072: CPR/ OBSAI Up to 10G Dual Channel

**OTN**

- MU110011A-001: Up to 10G Single Channel
- MU110011A-003: Up to 10G Dual Channel
- MU110011A-053: OTN 40G Single Channel
- MU110011A-054: OTN 40G Dual Channel
- MU110011A-055: OTN 100G Single Channel
- MU110011A-083: STM-256 OC-768 Single Channel
- MU110011A-084: STM-256 OC-768 Dual Channel

**Fibre Channel**

- MU110011A-005: Up to 10G FC Single Channel
- MU110011A-004: Up to 10G FC Dual Channel

### MU110013A

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MU110013A-001</td>
<td>Up to 40G Single Channel</td>
</tr>
<tr>
<td>MU110013A-003</td>
<td>Ethernet 40G Single Channel</td>
</tr>
<tr>
<td>MU110013A-013</td>
<td>Ethernet 100G Single Channel</td>
</tr>
<tr>
<td>MU110013A-015</td>
<td>Ethernet 100G Dual Channel</td>
</tr>
</tbody>
</table>

**CPR/ OBSAI**

- MU110013A-071: CPR/ OBSAI Up to 10G Single Channel
- MU110013A-072: CPR/ OBSAI Up to 10G Dual Channel

**OTN**

- MU110013A-001: Up to 40G Single Channel
- MU110013A-003: Up to 10G Dual Channel
- MU110013A-053: OTN 40G Single Channel
- MU110013A-055: OTN 100G Single Channel
- MU110013A-063: 40G/100G ODU Multi Stage

**Fibre Channel**

- MU110013A-005: Up to 100G FC Single Channel
- MU110013A-004: Up to 10G FC Dual Channel

### Device Test

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MU110013A-008</td>
<td>4 x 25G/28G BERT</td>
</tr>
</tbody>
</table>

*7: These options including MU11001xA-061 function. MU110013A does not have a physical interface of these options. These options are required for the client signal mapped in the OTN. Refer to page 5 to 7 for OTN and client signals.

*8: Required to MU110013A-015 or MU110013A-016.
## 5. Optional Accessories

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Optical modules</strong></td>
<td></td>
</tr>
<tr>
<td>G0332A</td>
<td>100M FX 1310 nm MM SFP</td>
</tr>
<tr>
<td>G0329A</td>
<td>10G LR 1310 nm SFP+</td>
</tr>
<tr>
<td>G0315A</td>
<td>10G LR/LW 1310 nm SFP+</td>
</tr>
<tr>
<td>G0316A</td>
<td>10G ER/EW 1550 nm 40 km SFP+</td>
</tr>
<tr>
<td>G0318A</td>
<td>Up to 2.7G 1310 nm 15 km SFP</td>
</tr>
<tr>
<td>G0320A</td>
<td>Up to 2.7G 1310 nm 40 km SFP</td>
</tr>
<tr>
<td>G0321A</td>
<td>Up to 2.7G 1550 nm 80 km SFP</td>
</tr>
<tr>
<td>G0328A</td>
<td>Up to 2.7G 1310 nm 80 km SFP</td>
</tr>
<tr>
<td>G0322A</td>
<td>1G/2G/4G FC 850 nm SFP</td>
</tr>
<tr>
<td>G0323A</td>
<td>1G/2G/4G FC 1550 nm SFP</td>
</tr>
<tr>
<td>G0356A</td>
<td>8G FC/10G SR 850 nm QSFP+</td>
</tr>
<tr>
<td>G0359A</td>
<td>40G SR4 850 nm QSFP+</td>
</tr>
<tr>
<td>G0334A</td>
<td>40G LR4 1310 nm QSFP+</td>
</tr>
<tr>
<td>G0335A</td>
<td>40G LR4 1310 nm CFP</td>
</tr>
<tr>
<td>G0336A</td>
<td>40G FR 1550 nm CFP</td>
</tr>
<tr>
<td>G0337A</td>
<td>100G LR4 1310 nm CFP</td>
</tr>
<tr>
<td>G0338A</td>
<td>100G LR4 1310 nm CFP</td>
</tr>
<tr>
<td>G0339A</td>
<td>100G LR4 1550 nm CFP</td>
</tr>
<tr>
<td>G0366A</td>
<td>100G BASE-SR4 10 lane Extender</td>
</tr>
<tr>
<td>G0364A</td>
<td>100G BASE-LR4 10 lane Extender</td>
</tr>
<tr>
<td>G0365A</td>
<td>100G BASE-LR4 10 lane Extender</td>
</tr>
<tr>
<td><strong>Mainframe optional accessories</strong></td>
<td></td>
</tr>
<tr>
<td>B0717A</td>
<td>Hard Case</td>
</tr>
<tr>
<td>Z1860A</td>
<td>Battery Charger</td>
</tr>
<tr>
<td>G0325A</td>
<td>GPS Receiver</td>
</tr>
<tr>
<td>G0382A</td>
<td>Autofocus Video Inspection Probe</td>
</tr>
<tr>
<td>G0306B</td>
<td>Video Inspection Probe</td>
</tr>
<tr>
<td>J1667A</td>
<td>GPIB-USB Converter</td>
</tr>
<tr>
<td>B0705A</td>
<td>Rack Mount Kit</td>
</tr>
<tr>
<td><strong>Interface convertor for optical module</strong></td>
<td></td>
</tr>
<tr>
<td>J1665A</td>
<td>CFP2-CFP4 Adaptor</td>
</tr>
<tr>
<td>J166B</td>
<td>CFP2-QSFP28 Adaptor</td>
</tr>
<tr>
<td><strong>Electrical interface for optical module</strong></td>
<td></td>
</tr>
<tr>
<td>MZ1223C</td>
<td>10 lane Extender</td>
</tr>
<tr>
<td>J1675A</td>
<td>SMP-SMA (male) Cable 40 cm</td>
</tr>
<tr>
<td>J1676A</td>
<td>SMP-SMP Cable 40 cm</td>
</tr>
<tr>
<td>J1677A</td>
<td>SMP-GPPO Cable 40 cm</td>
</tr>
<tr>
<td>J1666A</td>
<td>4 Lanes Extender for CFP2</td>
</tr>
<tr>
<td>J1669A</td>
<td>K (female)-GPPO Cable 5 cm</td>
</tr>
<tr>
<td>J1670A</td>
<td>V (female)-GPPO Cable 5 cm</td>
</tr>
<tr>
<td>J1672A</td>
<td>V (male)-GPPO Cable 40 cm</td>
</tr>
<tr>
<td>J1661A</td>
<td>GPPO-GPPO Cable 40 cm</td>
</tr>
<tr>
<td><strong>Cables</strong></td>
<td></td>
</tr>
<tr>
<td>J1571A</td>
<td>Optical Cable SM LC/PC to SC/PC 3 m</td>
</tr>
<tr>
<td>J1575A</td>
<td>Optical Cable SM LC/PC to FC/PC 3 m</td>
</tr>
<tr>
<td>J1579A</td>
<td>Optical Cable SM LC/PC to LC/PC 3 m</td>
</tr>
<tr>
<td>J1581A</td>
<td>Optical Cable MM LC/PC to LC/PC 3 m</td>
</tr>
<tr>
<td>J1583A</td>
<td>Optical Attenuator 10 dB LC/PC to LC/PC</td>
</tr>
<tr>
<td>J1584A</td>
<td>RJ45 Cable 3 m</td>
</tr>
<tr>
<td>J1585A</td>
<td>RJ48 to Crocodile Clips Cable 3 m</td>
</tr>
<tr>
<td>J1586A</td>
<td>RJ48 to Crocodile Clips Cable 20 dB ATT 3 m</td>
</tr>
<tr>
<td>J1588A</td>
<td>BNC Cable 2.5 m</td>
</tr>
<tr>
<td>J1589A</td>
<td>BNC to 1.6/5.6 Cable 2.5 m</td>
</tr>
<tr>
<td>J1591A</td>
<td>RJ48 to Two 3-pin Banana Plug Cable 2.5 m</td>
</tr>
<tr>
<td>J1597A</td>
<td>RJ48 Balanced PDH Cable Crossed 3 m</td>
</tr>
<tr>
<td>J1598A</td>
<td>Bantam Cable 3 m</td>
</tr>
<tr>
<td>J1571D</td>
<td>Coaxial Cord, 2.5 m (75Ω)</td>
</tr>
</tbody>
</table>

## 6. Extended Warranties

<table>
<thead>
<tr>
<th>Model/Order No.</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Manuals</strong></td>
<td></td>
</tr>
<tr>
<td>W3735AE</td>
<td>MT1100A Operation Manual (English)</td>
</tr>
<tr>
<td>W3735AW</td>
<td>MT1100A Operation Manual (Japanese)</td>
</tr>
<tr>
<td>W3736AE</td>
<td>MT1000A/MT1100A Remote Scripting Operation Manual (English)</td>
</tr>
<tr>
<td>W3736AW</td>
<td>MT1000A/MT1100A Remote Scripting Operation Manual (Japanese)</td>
</tr>
<tr>
<td>Z1578A</td>
<td>Z1223C Operation Manual (CD-ROM)</td>
</tr>
</tbody>
</table>

*10: Up to 4 SFP+/SFPs can be stored.  
*11: J1667A is required for SCPI remote control via GPIB.  
*12: CFP Interface.  
*13: CFP Interface. Supplied with Z1578A.  
*14: Use J1675A, J1676A or J1677A when connecting to the DUT. If the cables other than J1675A, J1676A or J1677A are used, the required performance may not be obtained.  
*15: Application parts for MZ1223C. Cables sold as single units.  
*16: Application parts for J1666A. Cable sold as single units.